

1. General description

Silicon Carbide MOSFET in a 3-lead TO247 plastic package, designed for high frequency, high efficiency systems.



2. Features and benefits

- Low on-resistance
- Fast switching speed
- 0V turn-off gate voltage for simple gate drive
- Easy to parallel
- Controllable dV/dt for optimized EMI
- Reduced cooling requirements
- RoHS compliant

3. Applications

- Switch Mode Power Supplies
- UPS
- Solar string inverter and solar optimizer
- EV Charger
- Motor Drives

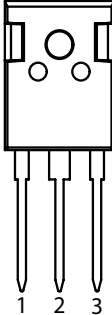
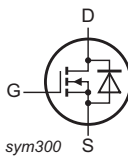
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Absolute maximum rating							
V_{DS}	drain-source voltage	$25\text{ °C} \leq T_j \leq 175\text{ °C}$		-	-	1200	V
I_D	drain current	$V_{GS} = 20\text{ V}; T_{mb} = 25\text{ °C}$		-	-	42	A
P_{tot}	total power dissipation	$T_{mb} = 25\text{ °C}$		-	-	230	W
T_j	junction temperature			-55	-	175	°C
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 20\text{ V}; I_D = 20\text{ A}; T_j = 25\text{ °C}$		-	80	98	mΩ
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	$I_D = 20\text{ A}; V_{DS} = 800\text{ V}; V_{GS} = 0V/20\text{ V}; T_j = 25\text{ °C}$		-	59	-	nC
Q_{GD}	gate-drain charge			-	11	-	nC
Source-drain diode							
Q_r	recovered charge	$I_{SD} = 20\text{ A}; di/dt = 500\text{ A}/\mu\text{s}; V_{DS} = 400\text{ V}; T_j = 25\text{ °C}$		-	108	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSCM80120W	TO247	WNSCM80120W6Q	Tube	30	SOT429	25-Mar-2013

7. Marking

Table 4. Marking codes

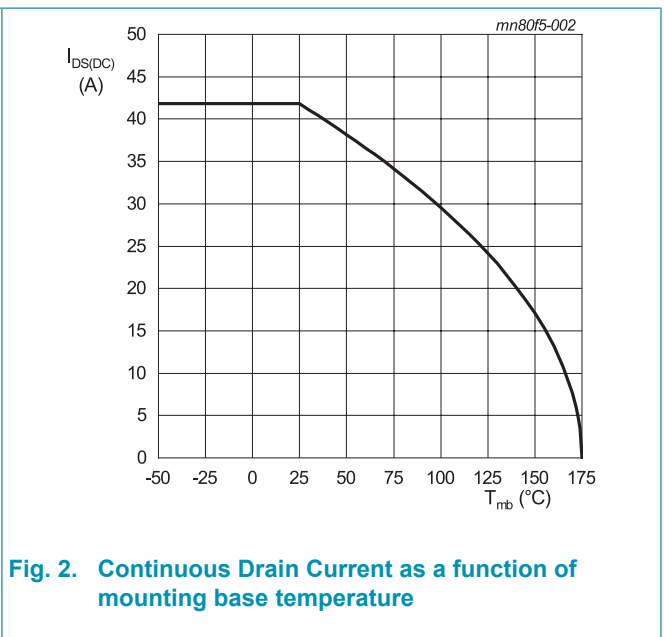
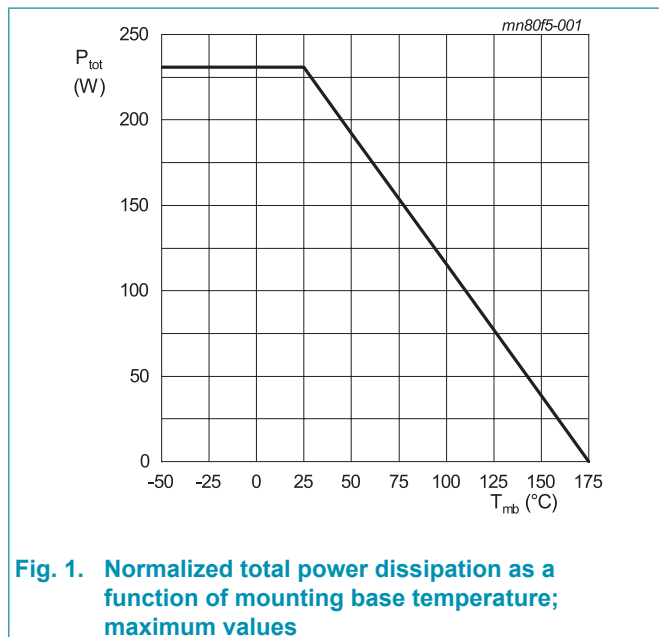
Type number	Marking codes
WNSCM80120W	WNSCM 80120W

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Min	Max	Unit
V_{DS}	drain-source voltage	$25\text{ °C} \leq T_j \leq 175\text{ °C}$		-	1200	V
$V_{GS,max}$	gate-source voltage			-10	25	V
$V_{GS,op}$	gate-source voltage			-5	20	V
P_{tot}	total power dissipation	$T_{mb} = 25\text{ °C}$		-	230	W
I_D	drain current	$V_{GS} = 20\text{ V}; T_{mb} = 25\text{ °C}$		-	42	A
		$V_{GS} = 20\text{ V}; T_{mb} = 100\text{ °C}$		-	29	A
I_{DM}	peak drain current	pulsed; $t_p \leq 10\text{ }\mu\text{s}; T_{mb} = 25\text{ °C}$		-	81	A
E_{as}	single pulse drain-to-source avalanche	$I_{AS} = 18\text{ A}; L = 1\text{ mH}; V_{DD} = 100\text{ V}; T_{j(init)} = 25\text{ °C}$		162	-	mJ
T_{stg}	storage temperature			-55	175	°C
T_j	junction temperature			-55	175	°C
$T_{sld(M)}$	peak soldering temperature			-	260	°C



9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	-	0.65	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M_d	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.
 Do not use self-tapping screws.
 Device is ESD sensitive. Handling precautions are recommended.

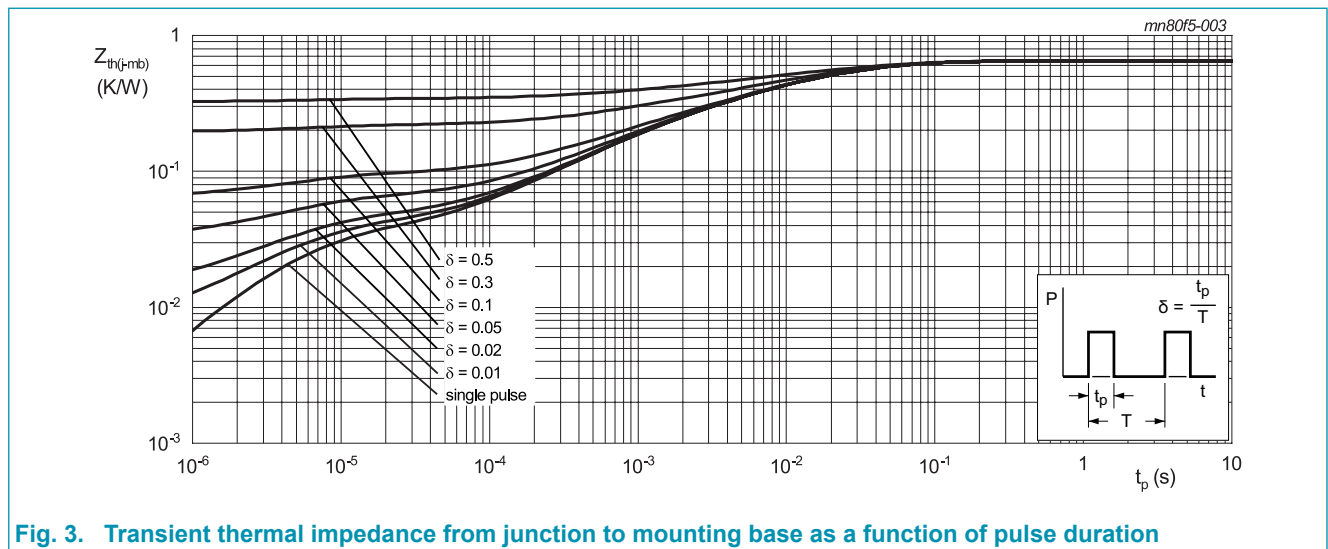
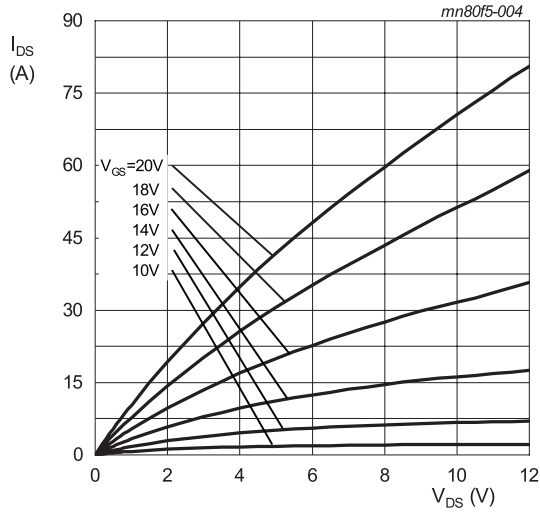


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

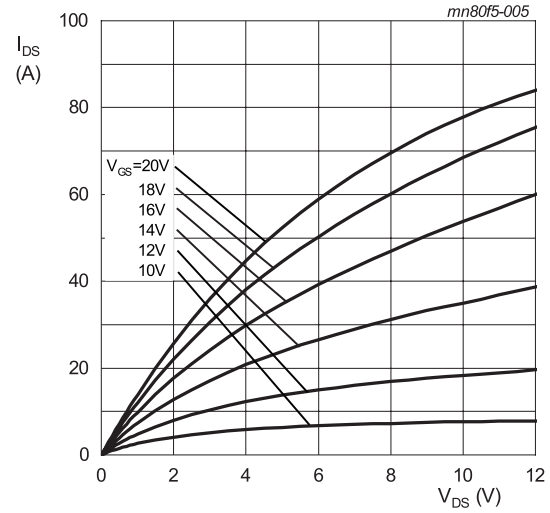
Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_J = 25 \text{ }^\circ C$		1200	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 6 \text{ mA}; V_{DS} = 10 \text{ V}; T_J = 25 \text{ }^\circ C$		2.5	3.5	4.5	V
		$I_D = 6 \text{ mA}; V_{DS} = 10 \text{ V}; T_J = 150 \text{ }^\circ C$		-	2.5	-	V
I_{DSS}	drain leakage current	$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_J = 25 \text{ }^\circ C$		-	0.1	100	μA
		$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_J = 150 \text{ }^\circ C$		-	1	-	μA
I_{GSS}	gate leakage current	$V_{GS} = 25 \text{ V}; V_{DS} = 0 \text{ V}; T_J = 25 \text{ }^\circ C$		-	10	100	nA
		$V_{GS} = -10 \text{ V}; V_{DS} = 0 \text{ V}; T_J = 25 \text{ }^\circ C$		-	10	100	nA
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 20 \text{ V}; I_D = 20 \text{ A}; T_J = 25 \text{ }^\circ C$		-	80	98	m Ω
		$V_{GS} = 20 \text{ V}; I_D = 20 \text{ A}; T_J = 150 \text{ }^\circ C$		-	110	-	m Ω
R_G	gate resistance	$f = 1 \text{ MHz}; T_J = 25 \text{ }^\circ C$		-	2.6	-	Ω
g_{fs}	transconductance	$V_{DS} = 20 \text{ V}; I_D = 20 \text{ A}; T_J = 25 \text{ }^\circ C$		-	8.8	-	S
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	$I_D = 20 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}/20 \text{ V}; T_J = 25 \text{ }^\circ C$		-	59	-	nC
Q_{GS}	gate-source charge			-	23	-	nC
Q_{GD}	gate-drain charge			-	11	-	nC
C_{iss}	input capacitance	$V_{DS} = 1000 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz}; T_J = 25 \text{ }^\circ C$		-	1350	-	pF
C_{oss}	output capacitance			-	68	-	pF
C_{rss}	reverse transfer capacitance			-	5.5	-	pF
E_{oss}	Coss stored energy			-	34	-	μJ
$t_{d(on)}$	turn-on delay time	$V_{DS} = 800 \text{ V}; V_{GS} = -5/20 \text{ V}; R_{G(ext)} = 0 \text{ } \Omega; I_D = 20 \text{ A}; L = 360 \text{ } \mu H; T_J = 25 \text{ }^\circ C$		-	12	-	ns
t_r	rise time			-	21	-	ns
$t_{d(off)}$	turn-off delay time			-	18	-	ns
t_f	fall time			-	18	-	ns
E_{on}	turn-on energy (SiC Diode FWD)			-	345	-	μJ
E_{off}	turn-off energy (SiC Diode FWD)			-	34	-	μJ
E_{on}	turn-on energy (Body Diode FWD)			-	520	-	μJ
E_{off}	turn-off energy (Body Diode FWD)			-	45	-	μJ
Source-drain diode							
V_{SD}	source-drain voltage	$V_{GS} = 0 \text{ V}; I_F = 10 \text{ A}; T_J = 25 \text{ }^\circ C$		-	4.1	-	V
		$V_{GS} = 0 \text{ V}; I_F = 10 \text{ A}; T_J = 150 \text{ }^\circ C$		-	3.5	-	V
t_{rr}	reverse recovery time	$I_{SD} = 20 \text{ A}; di/dt = 500 \text{ A}/\mu s; V_{DS} = 400 \text{ V}; T_J = 25 \text{ }^\circ C$		-	36	-	ns
Q_r	recovered charge			-	108	-	nC
I_{rrm}	reverse recovery current			-	5.1	-	A



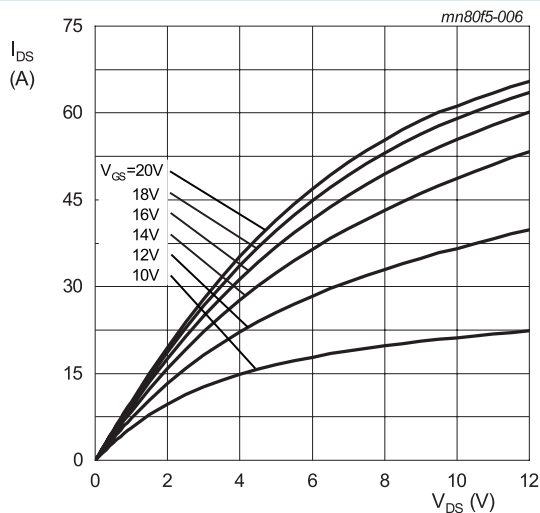
$T_j = -55\text{ °C}; t_p < 200\ \mu\text{s}$

Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



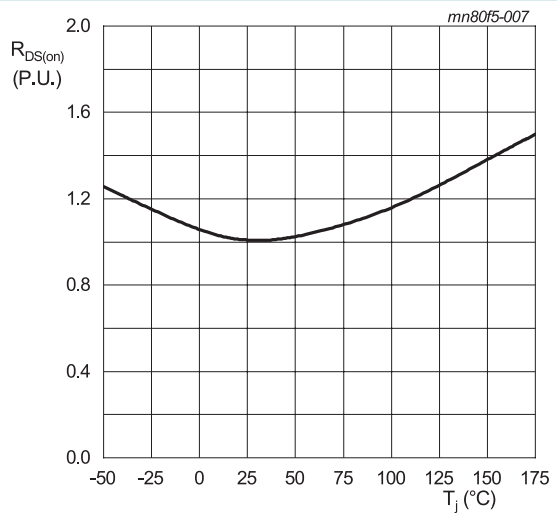
$T_j = 25\text{ °C}; t_p < 200\ \mu\text{s}$

Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



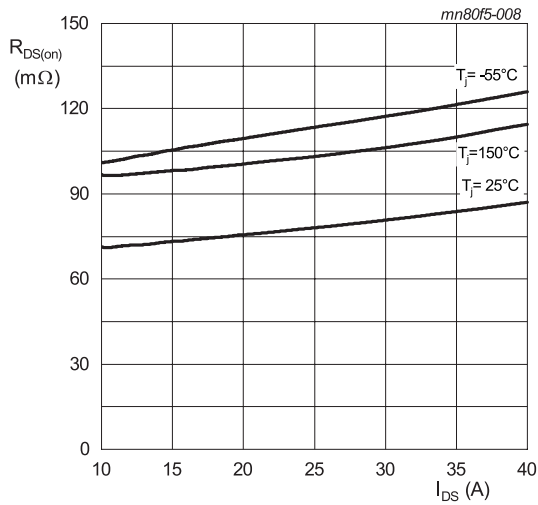
$T_j = 150\text{ °C}; t_p < 200\ \mu\text{s}$

Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

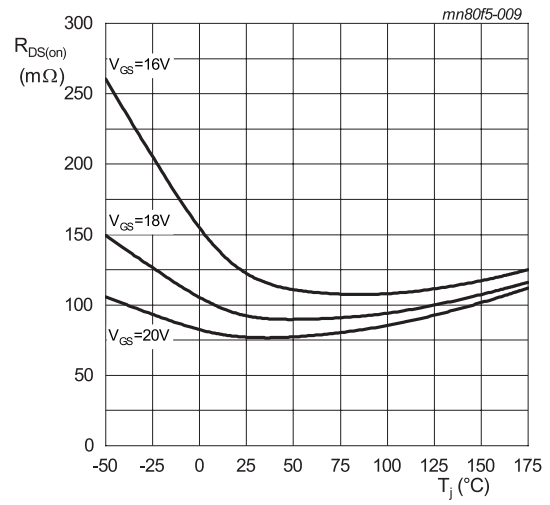


$I_{DS} = 20\text{ A}; V_{GS} = 20\text{ V}; t_p < 200\ \mu\text{s}$

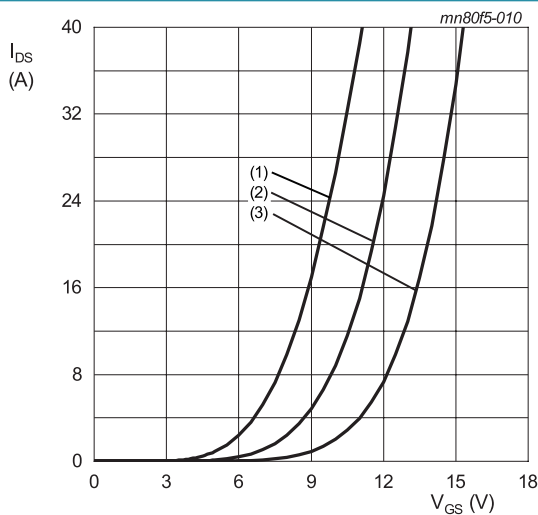
Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature



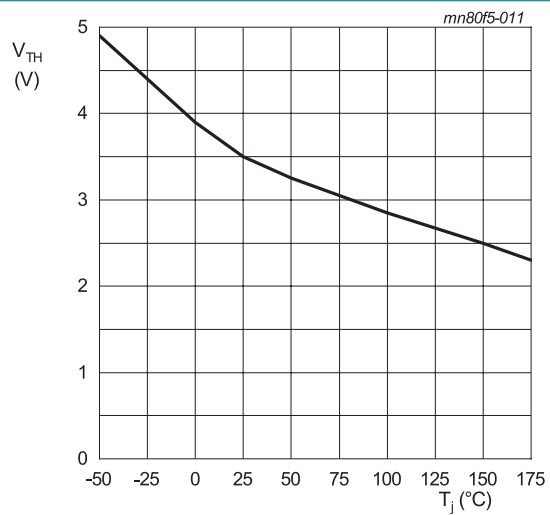
$V_{GS} = 20\text{ V}; t_p < 200\ \mu\text{s}$
Fig. 8. Drain-source on-state resistance as a function of drain current; typical values



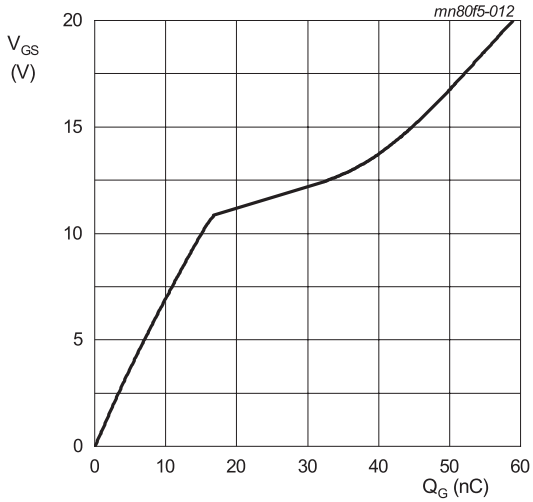
$I_{DS} = 20\text{ A}; t_p < 200\ \mu\text{s}$
Fig. 9. Drain-source on-state resistance as a function of junction temperature



$V_{DS} = 20\text{ V}; t_p < 200\ \mu\text{s}$
 (1) $T_J = 150^\circ\text{C}$
 (2) $T_J = 25^\circ\text{C}$
 (3) $T_J = -55^\circ\text{C}$
Fig. 10. Transfer characteristics; drain current as a function of gate-source voltage; typical values



$V_{DS} = 20\text{ V}; I_{DS} = 6\text{ mA}$
Fig. 11. Threshold voltage as a function of junction temperature



$I_{DS} = 20 \text{ A}; I_{GS} = 1 \text{ mA}; V_{DS} = 800 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$
Fig. 12. Gate-source voltage as a function of gate charge; typical values

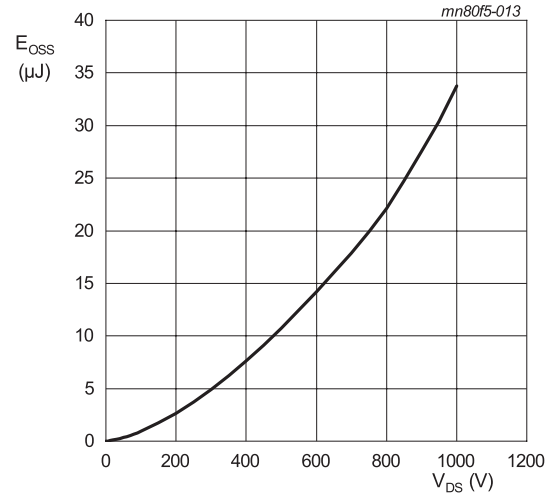
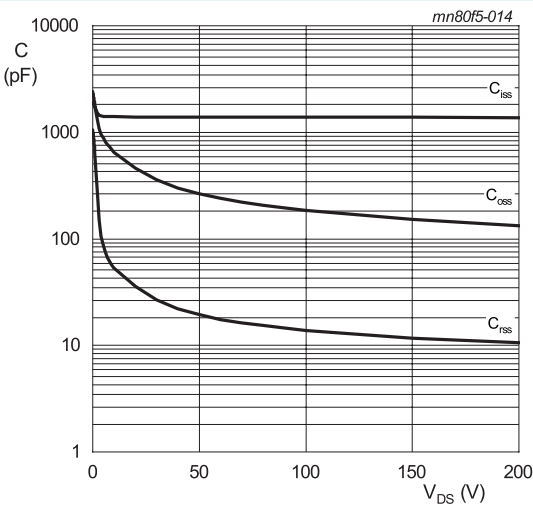
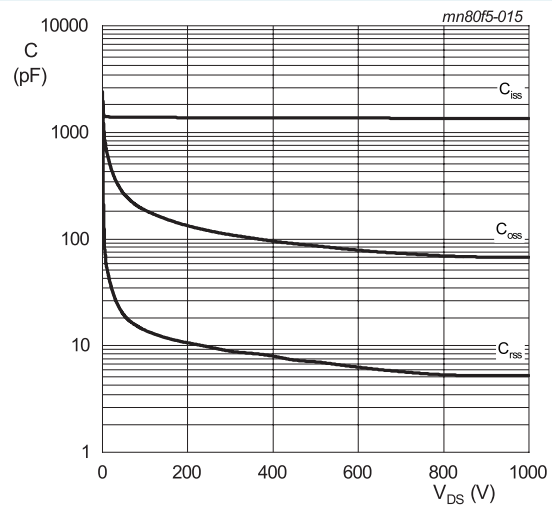


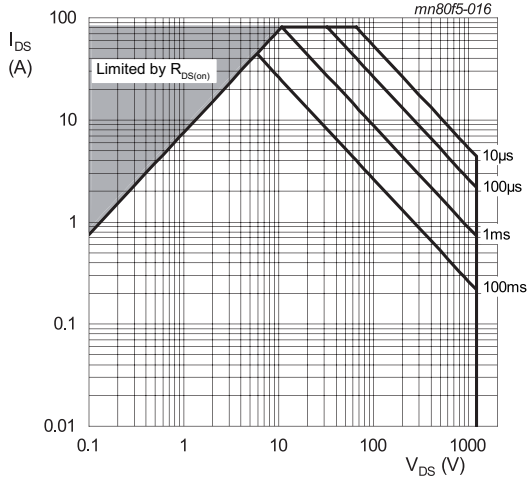
Fig. 13. Output capacitor stored energy as a function of drain-source voltage



$V_{DS} = 0 - 200 \text{ V}$
 $T_j = 25 \text{ }^\circ\text{C}; V_{AC} = 25 \text{ mV}; f = 1 \text{ MHz}$
Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

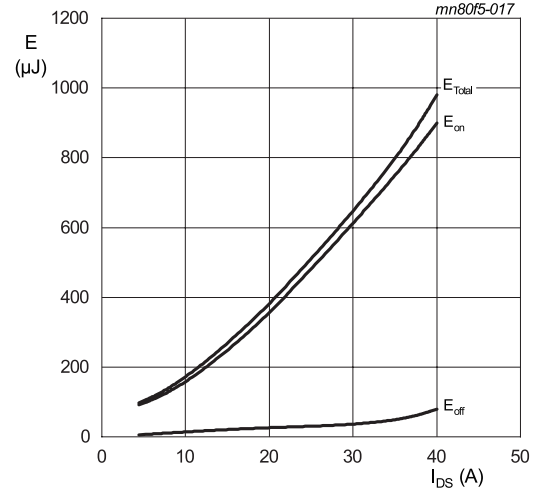


$V_{DS} = 0 - 1000 \text{ V}$
 $T_j = 25 \text{ }^\circ\text{C}; V_{AC} = 25 \text{ mV}; f = 1 \text{ MHz}$
Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



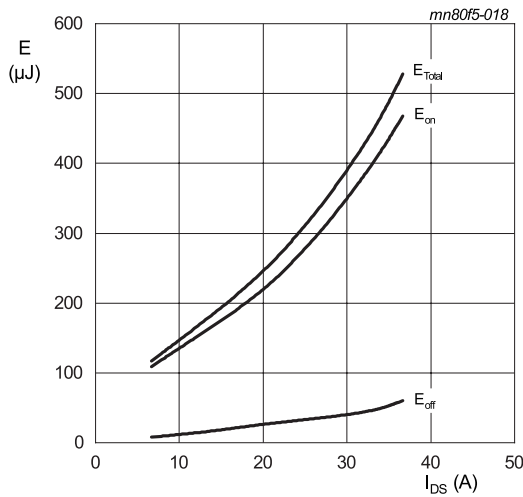
$T_c = 25\text{ }^\circ\text{C}$; $D = 0$
Parameter: t_p

Fig. 16. Forward bias safe operating area



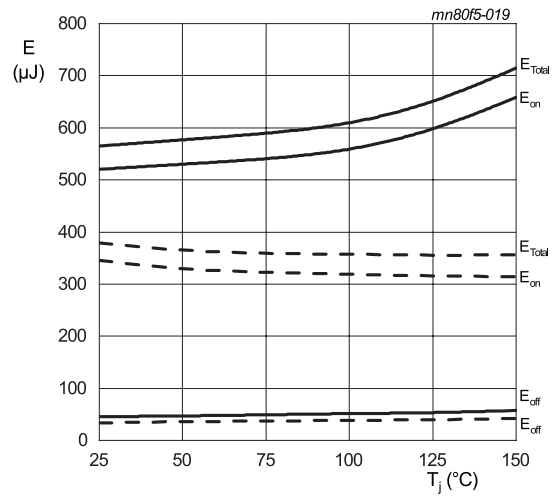
$T_j = 25\text{ }^\circ\text{C}$; $V_{DD} = 800\text{ V}$; $R_{G(ext)} = 0\text{ }\Omega$; $V_{GS} = -5\text{V}/20\text{ V}$
FWD = WNSC2D101200W; $L = 360\text{ }\mu\text{H}$

Fig. 17. Clamped Inductive Switching Energy as a function of drain current



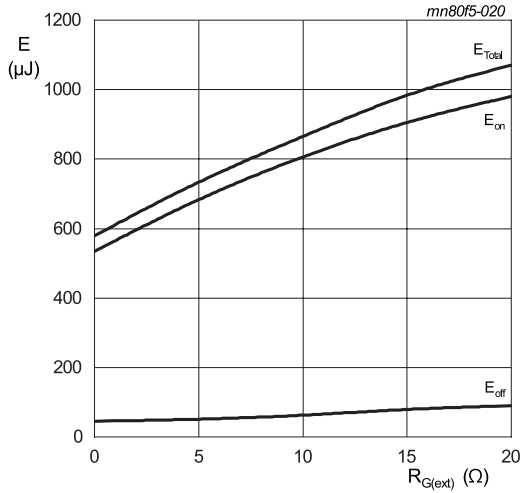
$T_j = 25\text{ }^\circ\text{C}$; $V_{DD} = 600\text{ V}$; $R_{G(ext)} = 0\text{ }\Omega$; $V_{GS} = -5\text{V}/20\text{ V}$
FWD = WNSC2D101200W; $L = 360\text{ }\mu\text{H}$

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



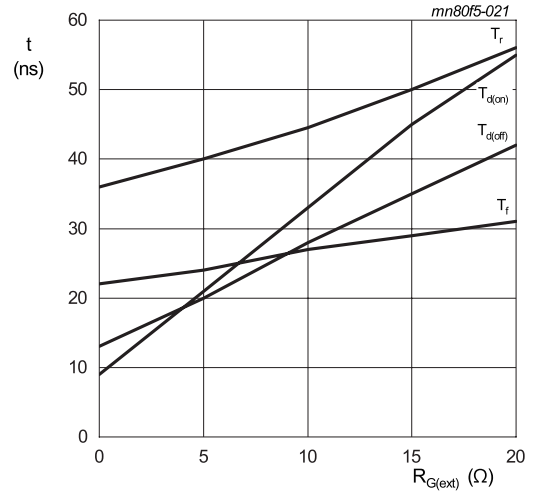
$I_{DS} = 20\text{ A}$; $V_{DD} = 800\text{ V}$; $R_{G(ext)} = 0\text{ }\Omega$; $V_{GS} = -5\text{V}/20\text{ V}$
 $L = 360\text{ }\mu\text{H}$
FWD = WNSCM80120W
FWD = WNSC2D101200W(- - -)

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



$T_j = 25\text{ }^\circ\text{C}$; $V_{DD} = 800\text{ V}$; $I_{DS} = 20\text{ A}$; $V_{GS} = -5\text{V}/20\text{ V}$
FWD = WNSCM80120W; $L = 360\text{ }\mu\text{H}$

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



$T_j = 25\text{ }^\circ\text{C}$; $V_{DD} = 800\text{ V}$; $I_{DS} = 20\text{ A}$; $V_{GS} = -5\text{V}/20\text{ V}$
FWD = WNSCM80120W; $L = 360\text{ }\mu\text{H}$

Fig. 21. Switching time as a function of external gate resistance

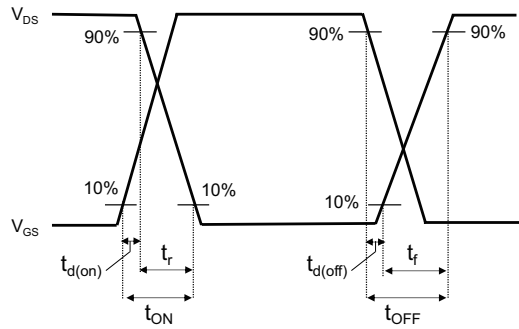
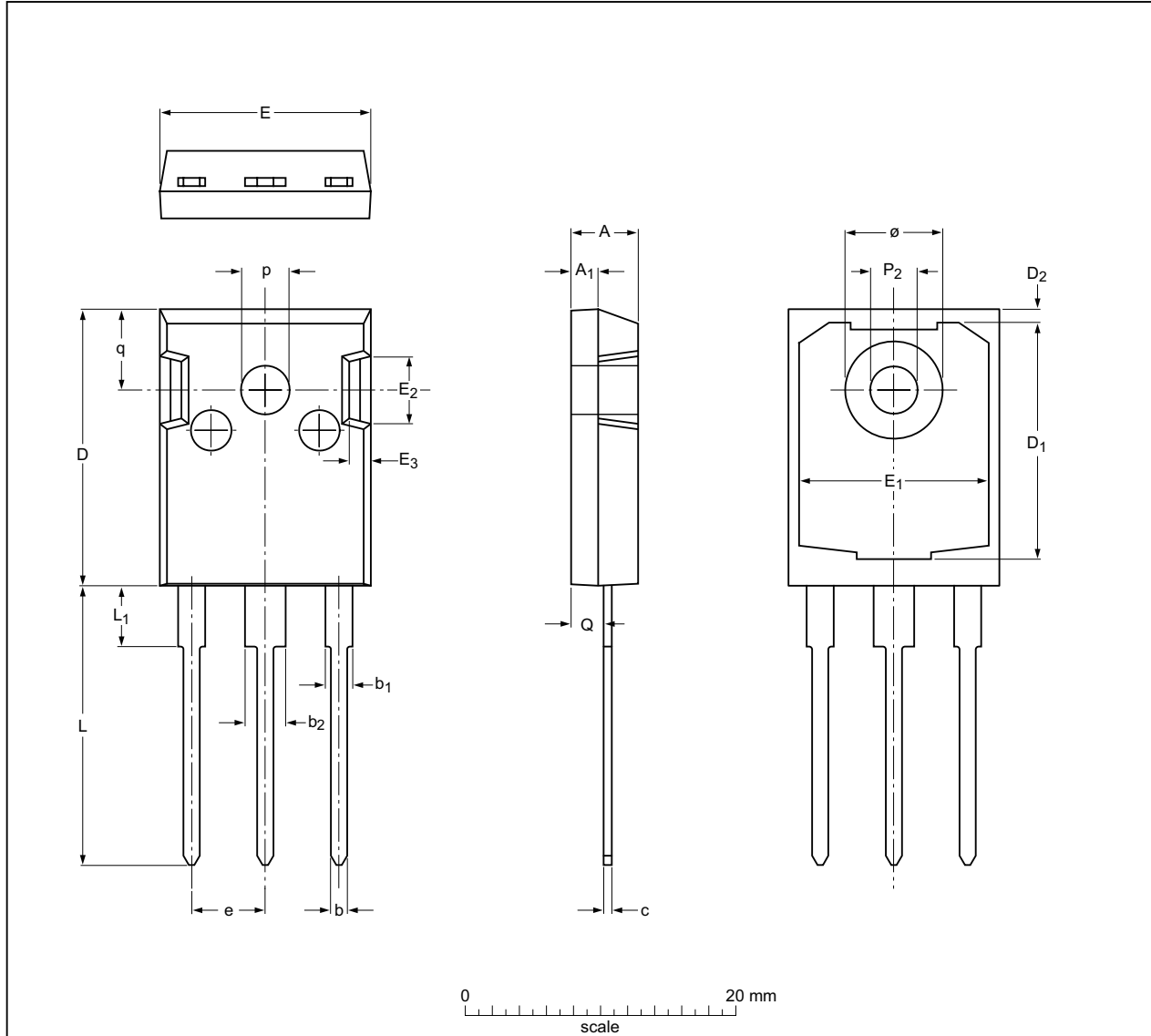


Fig. 22. Switching time definition

11. Package outline

Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247

SOT429



Dimensions (mm are the original dimensions)

Unit ⁽¹⁾	A	A ₁	b	b ₁	b ₂	c	D	D ₁	D ₂	E	E ₁	E ₂	E ₃	e ⁽¹⁾	L	L ₁	P ₂	p	Q	q	ø	
max	5.20	2.10	1.40	2.20	3.20	0.70	20.6	17.68	1.20	15.75	14.22	5.20	1.80	5.45	20.90	4.75	3.60	3.70	2.60	6.18	7.30	
nom																						
min	4.70	1.90	1.00	1.80	2.80	0.50	20.3	17.28	0.80	15.45	13.82	4.80	1.40		20.40	4.25	3.40	3.50	2.20	5.78	7.10	

Note

1. Basic spacing between centers.

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Outline version	References			European projection	Issue date
	IEC	JEDEC	JEITA		
SOT429	TO-247				04-09-14 13-03-25

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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