

WMS20N270SK N-Channel Silicon MOSFET Rev.02 - 28 June 2024

Product data sheet

1. General description

WMS20N270SK is a high performance super logic level N-channel MOSFET in SOT23 package, which utilizes advanced Trench MOSFET technology to provide low $R_{DS(on)}$ and gate charge. It is designed and qualified in a wide range of industrial and consumer applications.



2. Features and benefits

- High ESD sensitivity devices
- Advance High Cell Density Trench Technology
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Capacitance to Minimize Switching Losses
- Optimized Gate Charge to Minimize Driver Losses
- RoHS Compliant, Halogen Free and Lead Free

3. Applications

- Load Switch
- General PWM Applications

4. Quick reference data

Table 1. Qu	uick reference data						
Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage			20			V
V_{GS}	gate-source voltage				±10		V
I _D	continuous drain current	V _{GS} = 4.5 V; T _a = 25 °C			6		А
P _{tot}	power dissipation	T _a = 25 °C			1.4		W
T _j	junction temperature			-55 to 150		0	°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						·
$R_{\text{DS(on)}}$	drain-source on-state	V_{GS} = 4.5 V, I _D = 5.7 A		-	20	27	mΩ
resistance		V _{GS} = 2.5 V, I _D = 3 A		-	28	44	mΩ
Dynamic	Dynamic characteristics						
Q _{G(tot)}	total gate charge	I_{D} = 5.7 A; V_{DS} = 10 V; V_{GS} = 4.5 V		-	5.3	-	nC

5. Pinning information

Table 2. P	able 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	G	gate	2	П				
2	S	source						
3	D	drain		G sym300 S				

6. Ordering information

Table 3. Ordering information						
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WMS20N270SK	SOT23	WMS20N270SKX	Reel	3000	SOT23L	22-Aug-2022

7. Marking

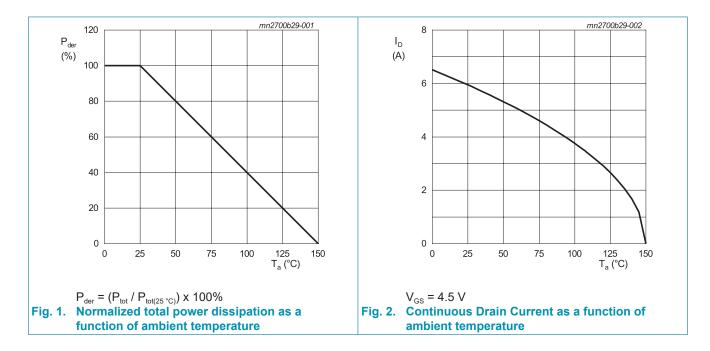
Table 4. Marking codes						
Type number	Marking codes					
WMS20N270SK	AD					

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage			20	V
V _{GS}	gate-source voltage			±10	V
I _D	continuous drain current	V _{GS} = 4.5 V; T _a = 25 °C		6	А
		V _{GS} = 4.5 V; T _a = 70 °C		4.8	А
I _{DM}	pulsed drain current	t _p = 10 μs; T _a = 25 °C		24	А
P _{tot}	power dissipation	T _a = 25 °C		1.4	W
T _{stg}	storage temperature			-55 to 150	°C
T _j	junction temperature			-55 to 150	°C

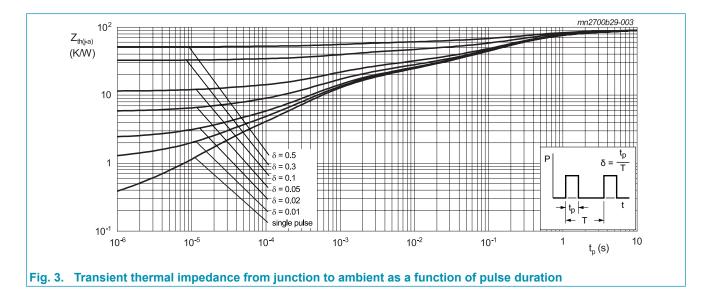


9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	t ≤ 10s	[1]	-	72	90	K/W
	from junction to ambient	in free air	[1]	-	95	120	K/W

[1] Surface mount on FR4 board of 1 inch², 1 oz copper.

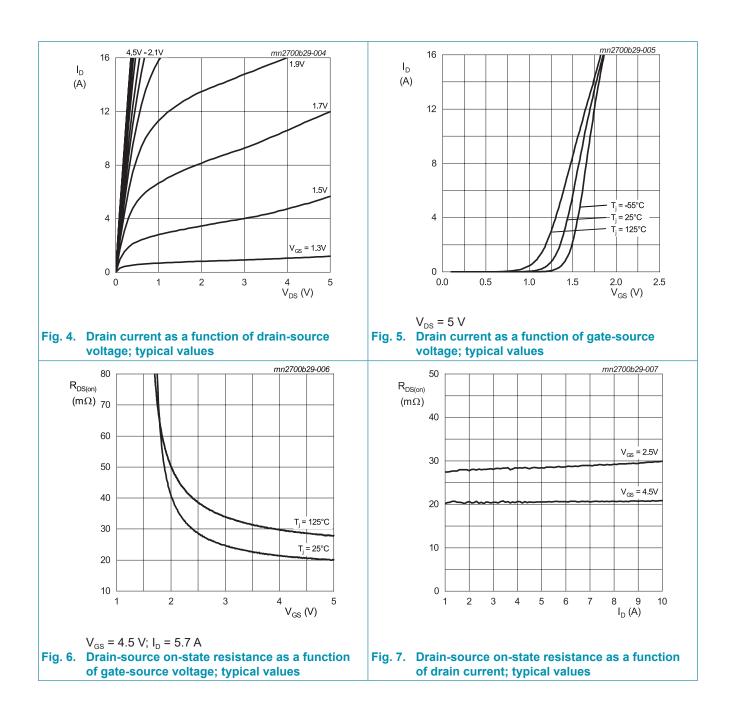


10. Characteristics

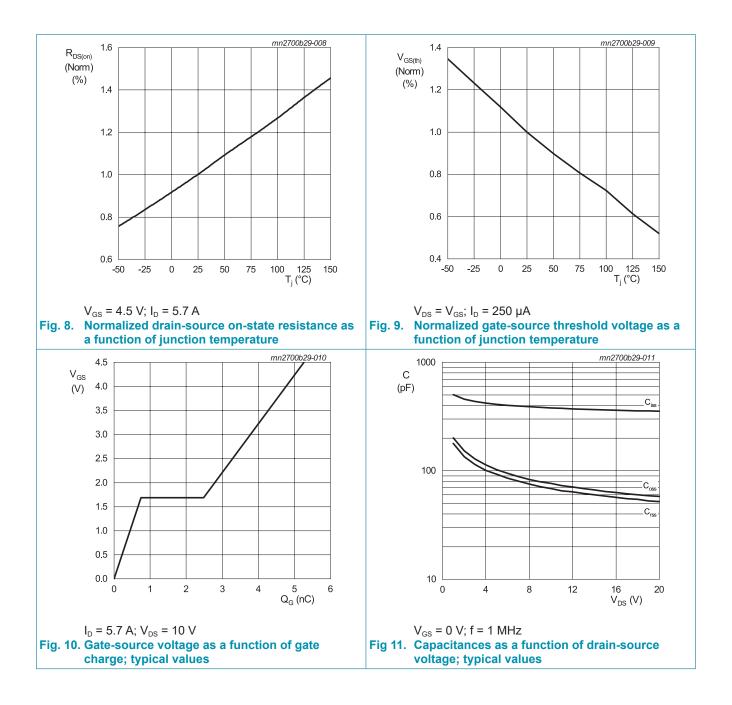
Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$V_{(\text{BR})\text{DSS}}$	drain-source breakdown voltage	I_{D} = 250 µA; V_{GS} = 0 V		20	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS}		0.5	0.7	1.1	V
I _{DSS}	drain leakage current	V _{DS} = 20 V; V _{GS} = 0 V		-	-	1	μA
		V_{DS} = 20 V; V_{GS} = 0 V; T_j = 125 °C		-	-	10	μA
I _{GSS}	gate leakage current	V _{GS} = ±10 V; V _{DS} = 0 V		-	-	±100	nA
$R_{\text{DS(on)}}$	drain-source on-state	V _{GS} = 4.5 V; I _D = 5.7 A		-	20	27	mΩ
	resistance	V _{GS} = 2.5 V; I _D = 3 A		-	28	44	mΩ
R _G	gate resistance	f = 1 MHz		-	3.3	-	Ω
Dynamic	characteristics	·					
Q _{G(tot)}	total gate charge	$I_{\rm D}$ = 5.7 A; $V_{\rm DS}$ = 10 V; $V_{\rm GS}$ = 4.5 V		-	5.3	-	nC
Q _{GS}	gate-source charge			-	0.7	-	nC
Q_{GD}	gate-drain charge			-	1.7	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; V _{GS} = 0 V; f = 1 MHz		-	381	-	pF
C _{oss}	output capacitance			-	77	-	pF
C _{rss}	reverse transfer capacitance			-	69	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 10 \text{ V}; V_{GS} = 4.5 \text{ V}; R_G = 6 \Omega;$		-	7.1	-	ns
t _r	rise time	$I_{\rm D} = 5.7 {\rm A}$		-	14	-	ns
$t_{\rm d(off)}$	turn-off delay time			-	25	-	ns
t _f	fall time			-	13	-	ns
Source-d	rain diode		-				
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 1 A		-	0.7	1	V
		V _{GS} = 0 V; I _S = 1 A; T _j = 125 °C		-	0.57	-	V
ls	body-diode continuous current	T _a = 25 °C		-	-	2	A
t _{rr}	reverse recovery time	V_{GS} = 0 V; I _S = 5.7 A; di/dt = 100 A/µs		-	13	-	ns
Q _{rr}	reverse recovered charge	1		-	3.2	-	nC
I _{rrm}	reverse recovery current			-	0.4	-	А

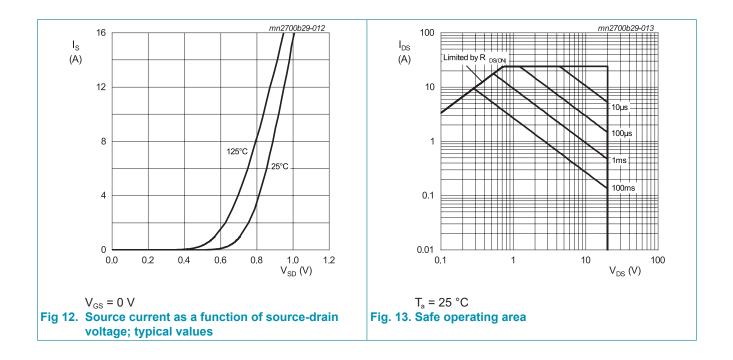
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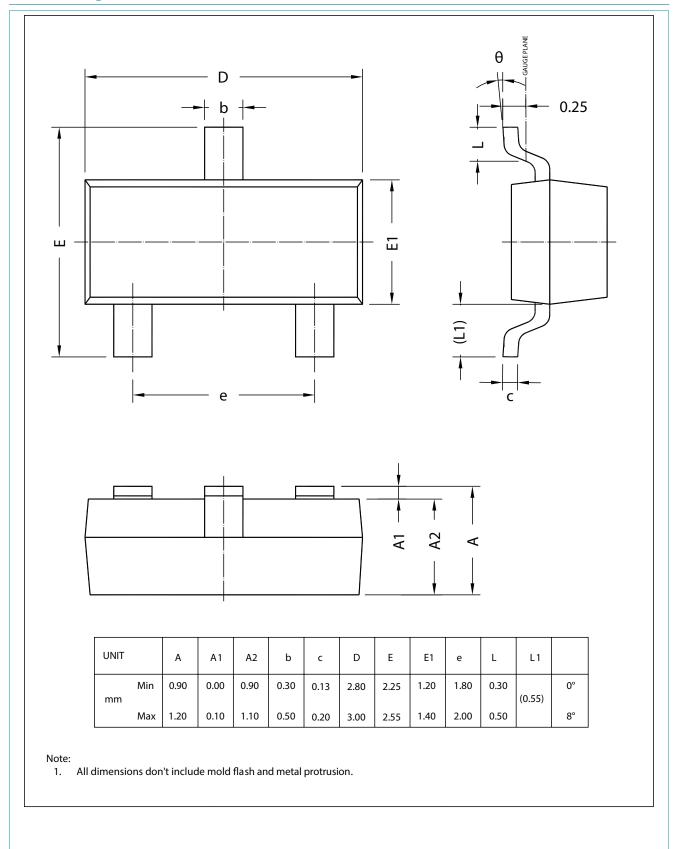
N-Channel Silicon MOSFET



WMS20N270SK N-Channel Silicon MOSFET



11. Package outline



WMS20N270SK

N-Channel Silicon MOSFET

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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