**Product data sheet** 

## 1. General description

WG40N120HFW1 uses advanced Fine Trench Field-stop IGBT technology with anti-parallel diode in TO-247 package. This device is part of the High speed series of IGBTs, which represents an optimum compromise between conduction and switching losses to maximize the efficiency of high switching frequency converter.



### 2. Features and benefits

- · Maximum junction temperature 175 °C
- · High switching speed
- Positive Temperature efficient for Easy Parallel Operating
- · Very soft, fast recovery anti-parallel diode
- · EMI Improved Design

## 3. Applications

- Solar inverter
- UPS
- · Welding converters
- PFC
- · Mid to high switching frequency applications

### 4. Quick reference data

#### Table 1. Quick reference data

| Symbol               | Parameter   |  | Notes | Value |     |     | Unit |
|----------------------|---|--|-------|-------|-----|-----|------|
| V <sub>CE</sub>      | Collector-emitter voltage, T <sub>j</sub> ≥ 25 °C                       |  |       | 1200  |     |     | V    |
| I <sub>C</sub>       | DC collector current, limited by $T_{j(max)}$<br>$T_C = 100  ^{\circ}C$ |  |       |       | 40  |     | Α    |
| Symbol               | Parameter   | Conditions   | Notes | Min   | Тур | Max | Unit |
| Static cha           | Static characteristics  |  |       |       |     |     |      |
| V <sub>CE(sat)</sub> | Collector-emitter saturation voltage                                    | $V_{GE} = 15 \text{ V}; I_{C} = 40 \text{ A}; T_{j} = 25 ^{\circ}\text{C}$ |       | -     | 2.2 | 2.8 | V    |

# 5. Pinning information

### **Table 2. Pinning information**

| Pin | Symbol | Description                           | Simplified outline | Graphic symbol |
|-----|--------|---------------------------------------|--------------------|----------------|
| 1   | G      | gate                                  |                    | •C             |
| 2   | С      | collector                             |                    |                |
| 3   | E      | emitter                               |                    |                |
| mb  | С      | mounting base; connected to collector | TO247              | G E<br>sym200  |

# 6. Ordering information

### **Table 3. Ordering information**

| Type number  | Package<br>Name | Orderable part number | Packing method | Small packing quantity | Package version | Package issue date |
|--------------|-----------------|-----------------------|----------------|------------------------|-----------------|--------------------|
| WG40N120HFW1 | TO247           | WG40N120HFW1Q         | Tube           | 30                     | SOT429          | 25-Mar-2013        |

## 7. Marking

### **Table 4. Marking codes**

| Type number  | Marking codes   |
|--------------|-----------------|
| WG40N120HFW1 | G40N120<br>HFW1 |

# 8. Limiting values

### **Table 5. Limiting values**

| Symbol               | Parameter   | Notes | Value       | Unit |
|----------------------|---|-------|-------------|------|
| V <sub>CE</sub>      | Collector-emitter voltage, T <sub>j</sub> ≥ 25 °C   |       | 1200        | V    |
| I <sub>c</sub>       | DC collector current, limited by $T_{j(max)}$<br>$T_{c}$ = 25 °C<br>$T_{c}$ = 100 °C  |       | 80<br>40    | А    |
| I <sub>C(puls)</sub> | Pulsed collector current, t <sub>p</sub> limited by T <sub>j(max)</sub>   |       | 120         | А    |
| -                    | Turn off safe operating area $V_{CE} \le 1200 \text{ V}, T_j \le 175 ^{\circ}\text{C}, t_p = 1  \mu\text{s}$  |       | 120         | А    |
| I <sub>F</sub>       | Diode forward current, limited by $T_{j(max)}$<br>$T_{C}$ = 25 °C<br>$T_{C}$ = 100 °C   |       | 80<br>40    | А    |
| I <sub>Fpuls</sub>   | Diode pulsed current, t <sub>p</sub> limited by T <sub>j(max)</sub>   |       | 120         | Α    |
| $V_{GE}$             | Gate-emitter voltage  |       | ±20         | V    |
| P <sub>tot</sub>     | Power dissipation $T_C = 25 ^{\circ}\text{C}$<br>Power dissipation $T_C = 100 ^{\circ}\text{C}$   |       | 750<br>375  | W    |
| t <sub>sc</sub>      | Short circuit withstand time $V_{GE} = 15.0 \text{ V}, V_{CC} \le 600 \text{ V}$ Allowed number of short circuits < 1000 Time between short circuits: $\ge 1.0 \text{ s}$ $T_j = 175^{\circ}\text{C}$ |       | 10          | us   |
| T <sub>stg</sub>     | Storage temperature   |       | -55 to +150 | °C   |
| T <sub>jmax</sub>    | Operating junction temperature  |       | 175         | °C   |
| -                    | Peak soldering temperture   |       | 260         | °C   |
| М                    | Mounting Torque with washer   |       | 0.55        | Nm   |

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

| Symbol               | Parameter                                      | Conditions | Notes | Min | Тур  | Max | Unit |
|----------------------|--|------------|-------|-----|------|-----|------|
| R <sub>th(j-c)</sub> | IGBT thermal resistance from junction to case  |            |       | -   | 0.20 | -   | K/W  |
| R <sub>th(j-c)</sub> | Diode thermal resistance from junction to case |            |       | -   | 0.72 | -   | K/W  |
| R <sub>th(j-a)</sub> | thermal resistance from junction to ambient    |            |       | -   | 40   | -   | K/W  |

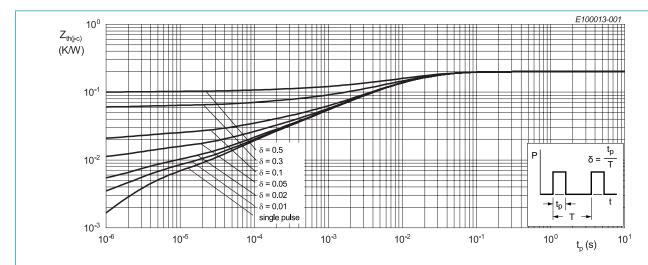


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; IGBT

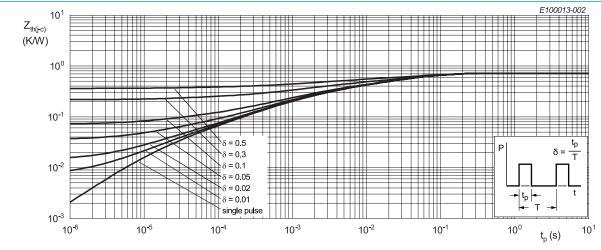


Fig. 2. Transient thermal impedance from junction to case as a function of pulse duration; Diode

## 10. Characteristics

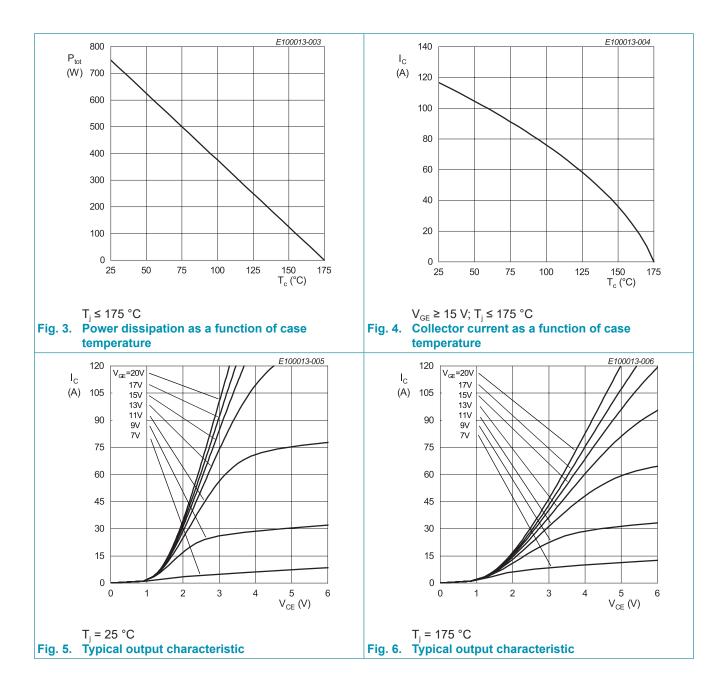
#### Table 7. Characteristics

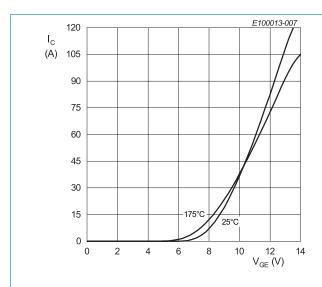
| Symbol               | Parameter                           | Conditions  | Notes | Min  | Тур  | Max | Unit |
|----------------------|-------------------------------------|---|-------|------|------|-----|------|
|                      | racteristics                        |   |       |      |      |     |      |
| BV <sub>CES</sub>    | Collector-emitter breakdown voltage | $V_{GE} = 0 \text{ V; } I_{C} = 1 \text{ mA}$   |       | 1200 | -    | -   | V    |
| V <sub>CE(sat)</sub> | Collector-emitter saturation        | $V_{GE}$ = 15 V; $I_{C}$ = 40 A; $T_{j}$ = 25 °C  |       | -    | 2.2  | 2.8 | V    |
|                      | voltage                             | V <sub>GE</sub> = 15 V; I <sub>C</sub> = 40 A; T <sub>j</sub> = 175 °C                        |       | -    | 3.0  | -   | V    |
| V <sub>F</sub>       | Diode forward voltage               | V <sub>GE</sub> = 0 V; I <sub>F</sub> = 40 A; T <sub>j</sub> = 25 °C                          |       | -    | 2.3  | -   | V    |
|                      |                                     | $V_{GE} = 0 \text{ V}; I_F = 40 \text{ A}; T_j = 175 ^{\circ}\text{C}$                        |       | -    | 2.1  | -   | V    |
| $V_{\text{GE(th)}}$  | Gate-emitter threhold voltage       | $I_{\rm C}$ = 0.5 mA; $V_{\rm CE}$ = $V_{\rm GE}$   |       | 4.2  | 5.3  | 6.4 | V    |
| I <sub>CES</sub>     | Zero gate voltage collector current | V <sub>CE</sub> = 1200 V; V <sub>GE</sub> = 0 V; T <sub>j</sub> = 25 °C                       |       | -    | -    | 250 | μΑ   |
|                      |                                     | V <sub>CE</sub> =1200 V;V <sub>GE</sub> = 0 V; T <sub>j</sub> = 175 °C                        |       | -    | -    | 10  | mA   |
| g <sub>fs</sub>      | Transconductance                    | V <sub>CE</sub> = 20 V; I <sub>C</sub> = 40 A   |       | -    | 21   | -   | S    |
| Dynamic              | characteristics                     |   |       |      |      |     |      |
| C <sub>ies</sub>     | Input capacitance                   | $V_{CE} = 30 \text{ V}; V_{GE} = 0 \text{ V}; f = 1 \text{ MHz};$                             |       | -    | 6672 | -   | pF   |
| C <sub>oes</sub>     | Output capacitance                  | T <sub>j</sub> = 25 °C  |       | -    | 137  | -   | pF   |
| C <sub>res</sub>     | Reverse transfer capacitance        |   |       | -    | 25   | -   | pF   |
| $Q_{G}$              | Gate charge                         | $V_{CC} = 960 \text{ V}; I_C = 40 \text{ A}; V_{GE} = 15 \text{ V};$<br>$T_j = 25 \text{ °C}$ |       | -    | 200  | -   | nC   |

# 11. Switching Characteristics

Table 8. Switching Characteristics, Inductive Load

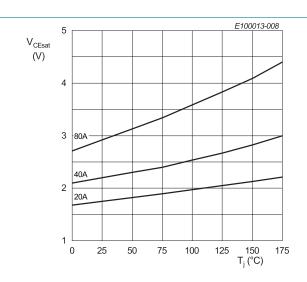
| Symbol              | Parameter                     | Conditions  | Notes | Min | Тур  | Max | Unit |
|---------------------|-------------------------------|---|-------|-----|------|-----|------|
| IGBT cha            | racteristics                  |   |       |     |      |     |      |
| $t_{d(on)}$         | Turn-on delay time            | T <sub>j</sub> = 25 °C;   |       | -   | 41   | -   | nS   |
| t <sub>r</sub>      | Rise time                     | $V_{CC}^{'} = 600 \text{ V}; I_{C}^{'} = 40 \text{ A}; V_{GE}^{'} = 15 \text{V} / 0 \text{V};$ $R_{G}^{'} = 3.6 \Omega$     |       | -   | 41   | -   | nS   |
| $t_{\text{d(off)}}$ | Turn-off delay time           |   |       | -   | 126  | -   | nS   |
| t <sub>f</sub>      | Fall time                     |   |       | -   | 68   | -   | nS   |
| E <sub>on</sub>     | Turn-on energy                |   |       | -   | 2.5  | -   | mJ   |
| E <sub>off</sub>    | Turn-off energy               |   |       | -   | 1    | -   | mJ   |
| E <sub>ts</sub>     | Total switching energy        |   |       | -   | 3.5  | -   | mJ   |
| t <sub>d(on)</sub>  | Turn-on delay time            | T <sub>j</sub> = 175 °C;  |       | -   | 41   | -   | nS   |
| t <sub>r</sub>      | Rise time                     | $V_{CC} = 600 \text{ V}; I_{C} = 40 \text{ A}; V_{GE} = 15 \text{V} / 0 \text{V};$ $R_{G} = 3.6 \Omega$                     |       | -   | 40   | -   | nS   |
| $t_{d(off)}$        | Turn-off delay time           |   |       | -   | 141  | -   | nS   |
| t <sub>f</sub>      | Fall time                     |   |       | -   | 106  | -   | nS   |
| E <sub>on</sub>     | Turn-on energy                |   |       | -   | 4    | -   | mJ   |
| E <sub>off</sub>    | Turn-off energy               |   |       | -   | 1.5  | -   | mJ   |
| E <sub>ts</sub>     | Total switching energy        |   |       | -   | 5.5  | -   | mJ   |
| Diode cha           | aracteristics                 |   | ı     |     | -    |     |      |
| t <sub>rr</sub>     | Reverse recovery time         | T <sub>j</sub> = 25 °C;   |       | -   | 200  | -   | nS   |
| Q <sub>r</sub>      | Reverse recovery charge       | $V_R = 600 \text{ V}; I_F = 40 \text{ A}; dI_F/dt = 500 \text{A/us}$  |       | -   | 1660 | -   | nC   |
| I <sub>RM</sub>     | Reverse recovery peak current |   |       | -   | 16   | -   | А    |
| t <sub>rr</sub>     | Reverse recovery time         | $T_j = 175 ^{\circ}\text{C};$<br>$V_R = 600 ^{\circ}\text{V}; I_F = 40 ^{\circ}\text{A}; dI_F/dt = 500 ^{\circ}\text{A/us}$ |       | -   | 453  | -   | nS   |
| Q <sub>r</sub>      | Reverse recovery charge       |   |       | -   | 5566 | -   | nC   |
| I <sub>RM</sub>     | Reverse recovery peak current |   |       | -   | 26   | -   | Α    |





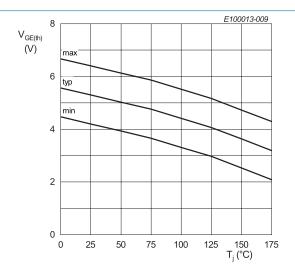
 $V_{CE} = 20 \text{ V}$ 

Fig. 7. Typical transfer characteristic



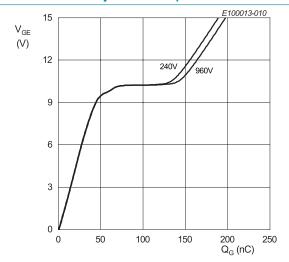
 $V_{GE} = 15 V$ 

Fig. 8. Typical collector-emitter saturation voltage as a function of junction temperature



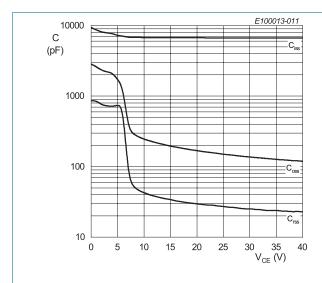
 $I_{c} = 500 \, \mu A$ 

Fig. 9. Gate-emitter threshold voltage as a function of junction temperature



 $I_{c} = 40 \text{ A}$ 

Fig. 10. Typical gate charge



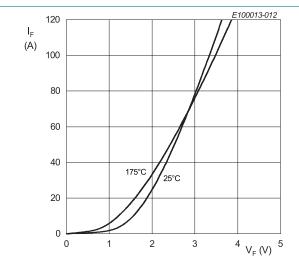
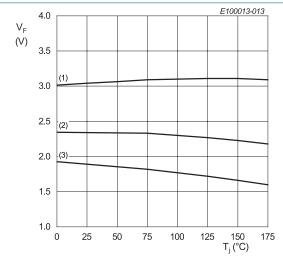
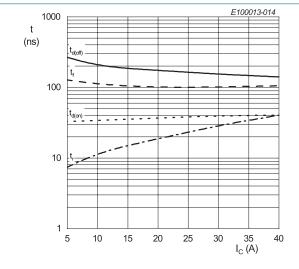


Fig. 12. Typical diode forward current as a function of forward voltage

 $\label{eq:VGE} V_{GE} = 0 \ V; \ f = 1 \ MHz$  Fig. 11. Typical capacitance as a function of collector-emitter voltage





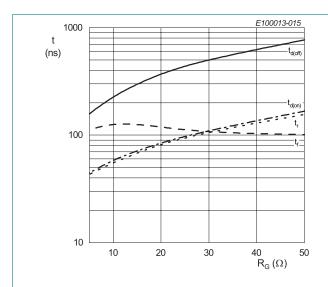
 $(1) I_F = 80 A$ (2)  $I_F = 40 \text{ A}$ 

(3)  $I_F = 20 A$ 

 $\rm R_g$  = 3.6  $\Omega;~\rm V_{GE}$  = 15V/0V;  $\rm T_j$  = 175 °C;  $\rm V_{CE}$  = 600 V; inductive load

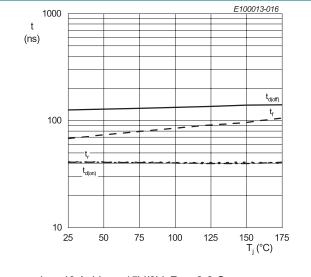
Fig. 13. Typical diode forward voltage as a function of junction temperature

Fig. 14. Typical switching times as a function of collector current



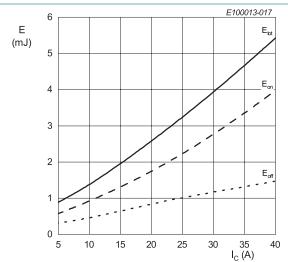
 $I_C$  = 40 A;  $V_{GE}$  = 15V/0V;  $T_j$  = 175 °C;  $V_{CE}$  = 600 V; inductive load

Fig. 15. Typical switching times as a function of gate resistance



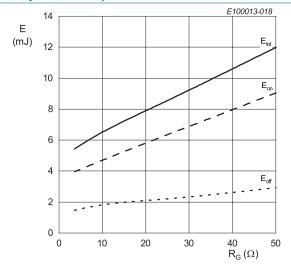
 $I_{C}$  = 40 A;  $V_{GE}$  = 15V/0V;  $R_{g}$  = 3.6  $\Omega$ ;  $V_{CE}$  = 600 V; inductive load

Fig. 16. Typical switching times as a function of junction temperature



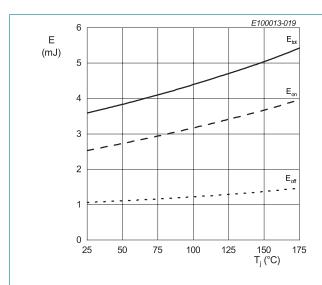
 $R_g = 3.6 \Omega$ ;  $V_{GE} = 15V/0V$ ;  $T_j = 175 ^{\circ}C$ ;  $V_{CE} = 600 V$ ; inductive load

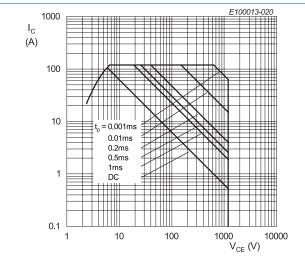
Fig. 17. Typical switching energy losses as a function of collector current



 $I_{C}$  = 40 A;  $V_{GE}$  = 15V/0V;  $T_{j}$  = 175 °C;  $V_{CE}$  = 600 V; inductive load

Fig. 18. Typical switching energy losses as a function of gate resistance





 $I_{C}$  = 40 A;  $V_{GE}$  = 15V/0V;  $R_{g}$  = 3.6  $\Omega;$   $V_{CE}$  = 600 V; inductive load

Fig. 20. Forward bias safe operating area

Fig. 19. Typical switching energy losses as a function of junction temperature

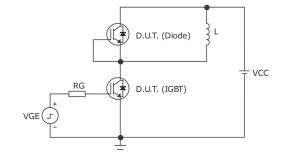


Fig. 21. Test circuit for inductive load switching

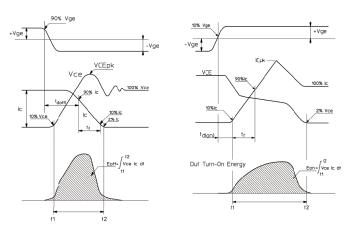
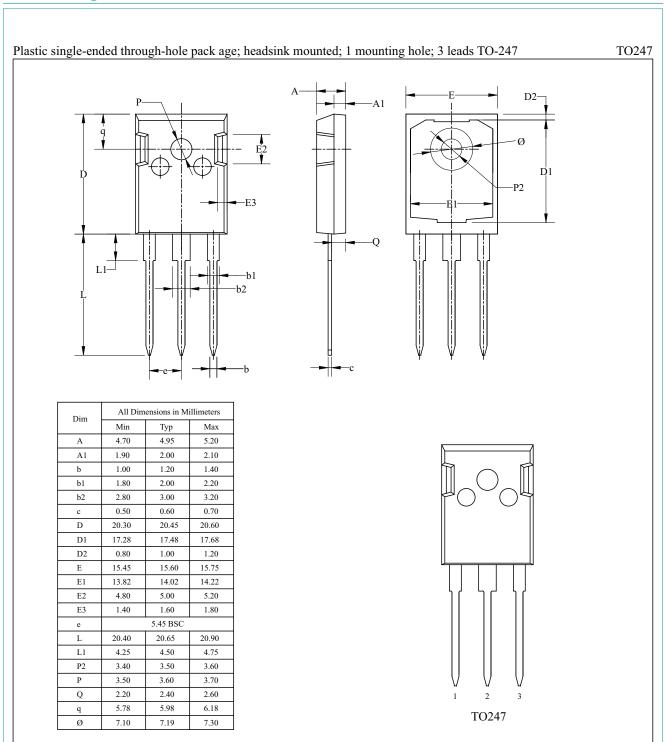


Fig. 22. Definition of switching times and losses

# 12. Package outline



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## 13. Legal information

#### Data sheet status

| Document status [1][2]               | Product status [3] | Definition  |
|--------------------------------------|--------------------|---|
| Objective<br>[short] data<br>sheet   | Development        | This document contains data from the objective specification for product development. |
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## 14. Contents

| 1. General description        | 1  |
|-------------------------------|----|
| 2. Features and benefits      | 1  |
| 3. Applications               | 1  |
| 4. Quick reference data       | 1  |
| 5. Pinning information        | 2  |
| 6. Ordering information       | 2  |
| 7. Marking                    | 2  |
| 8. Limiting values            | 3  |
| 9. Thermal characteristics    | 4  |
| 10. Characteristics           | 5  |
| 11. Switching Characteristics | 6  |
| 12. Package outline           | 12 |
| 13. Legal information         |    |
| 14. Contents                  |    |

For more information, please visit: http://www.ween-semi.com For sales office addresses, please send an email to: salesaddresses@ween-semi.com Date of release: 19 June 2024

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