**Product data sheet** 

### 1. General description

WSJM65R120B is a high voltage N-channel MOSFET in TO263 package, which utilizes the advanced super-junction technology to provide superior FOM  $R_{\rm DS(on)}\ ^* Q_{\rm g}$  among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.





### 2. Features and benefits

- Superior FOM R<sub>DS(on)</sub> \* Q<sub>g</sub>
- Extremely low switching loss
- 100% avalanche tested

### 3. Applications

- · Server power
- LEV charger
- LED power
- Adapters

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit	
Absolute	Absolute maximum rating							
V <sub>DS</sub>	drain-source voltage				650		V	
$V_{GS}$	gate-source voltage				±30		V	
I <sub>D</sub>	continuous drain current	T <sub>mb</sub> = 25 °C			30		А	
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C			272		W	
T <sub>j</sub>	junction temperature				-55 to 15	0	°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Static cha	aracteristics		,					
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}, I_{D} = 15 \text{ A}$		-	105	120	mΩ	
Dynamic	Dynamic characteristics							
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 15 A; V <sub>DS</sub> = 400 V; V <sub>GS</sub> = 10 V		-	54	-	nC	
E <sub>oss</sub>	coss stored erergy	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 0 to 400 V		-	6.6	-	μJ	

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	D	drain		
3	S	source		G (EX)
mb	D	mounting base; connected to drain	1 3	svm300 S

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WSJM65R120B	TO263	WSJM65R120BJ	Reel	800	TO263d	17-Mar-2023

## 7. Marking

#### Table 4. Marking codes

Type number	Marking codes
WSJM65R120B	WSJM 65R120B

# 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage			650	V
V <sub>GS</sub>	gate-source voltage			±30	V
I <sub>D</sub>	continuous drain current	T <sub>mb</sub> = 25 °C		30	Α
		T <sub>mb</sub> = 100 °C		19	Α
I <sub>DM</sub>	pulsed drain current	T <sub>mb</sub> = 25 °C		120	Α
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C		272	W
E <sub>AS</sub>	single pulse drain-to- source avalanche	$I_{AS} = 8.4 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 ^{\circ}\text{C}$		352	mJ
E <sub>AR</sub>	repetitive avalanche energy	$I_{AS} = 8.4 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		1.48	mJ
I <sub>AS</sub>	avalanche current, single pulse			8.4	А
dv/dt	MOSFET dv/dt ruggedness			50	V/ns
dv/dt	reverse diode dv/dt			10	V/ns
dl <sub>F</sub> /dt	maximum diode commutation speed			500	A/µs
T <sub>stg</sub>	storage temperature			-55 to 150	°C
T <sub>j</sub>	junction temperature			-55 to 150	°C

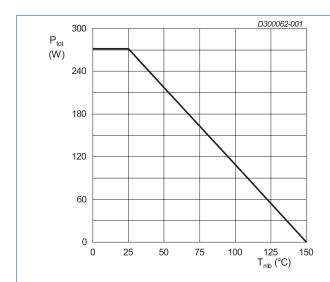


Fig. 1. Total power dissipation as a function of mounting base temperature

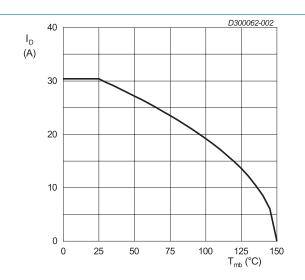


Fig. 2. Continuous Drain Current as a function of mounting base temperature

### 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	0.35	0.46	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air		-	60	-	K/W

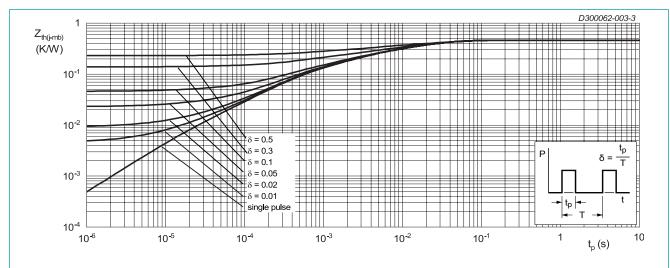


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

### 10. Characteristics

#### **Table 7. Characteristics**

T<sub>i</sub> = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V$		650	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}$		2.5	-	4.5	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 650 V; V <sub>GS</sub> = 0 V		-	-	1	μA
		$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 ^{\circ}\text{C}$		-	-	10	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A}$		-	105	120	mΩ
$R_G$	gate resistance	f = 1 MHz		-	1.7	-	Ω
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 15 A; V <sub>DS</sub> = 400 V; V <sub>GS</sub> = 10 V		-	54	-	nC
Q <sub>GS</sub>	gate-source charge			-	15	-	nC
$Q_{GD}$	gate-drain charge	,		-	20	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 400 V; V <sub>GS</sub> = 0 V; f = 1 MHz		-	2402	-	pF
C <sub>oss</sub>	output capacitance			-	56	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	3.4	-	pF
$C_{o(er)}$	effective output capacitance, energy related	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 0 to 400 V		-	83	-	pF
$C_{o(tr)}$	effective output capacitance, time related			-	415	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}; R_G = 10 \Omega;$		-	48	-	ns
t <sub>r</sub>	rise time	I <sub>D</sub> = 15 A		-	11	-	ns
$t_{d(off)}$	turn-off delay time			-	97	-	ns
t <sub>f</sub>	fall time			-	11	-	ns
Source-d	rain diode						
V <sub>SD</sub>	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>S</sub> = 15 A		-	8.0	1.1	V
I <sub>s</sub>	body-diode continuous current	T <sub>mb</sub> = 25 °C		-	-	30	Α
t <sub>rr</sub>	reverse recovery time	$V_R = 400 \text{ V}; I_F = 15 \text{ A}; dI_F/dt = 100 \text{ A/}\mu\text{s}$		-	376	-	ns
Q <sub>rr</sub>	reverse recovered charge			-	6.3	-	μC
I <sub>rrm</sub>	reverse recovery current			-	32	-	Α

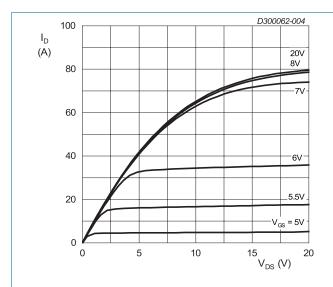
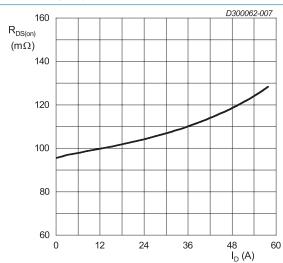
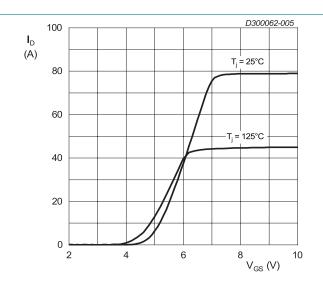


Fig. 4. Drain current as a function of drain-source voltage; typical values



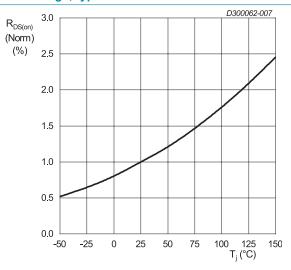
V<sub>GS</sub> = 10 V

Fig. 6. Drain-source on-state resistance as a function of drain current; typical values



V<sub>DS</sub> = 20 V

Fig. 5. Drain current as a function of gate-source voltage; typical values



V<sub>GS</sub> = 10 V; I<sub>D</sub> = 15 A

Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature

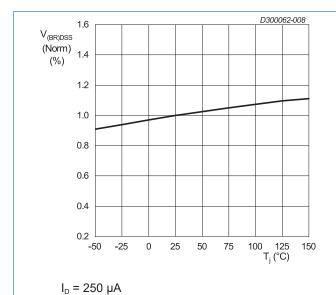
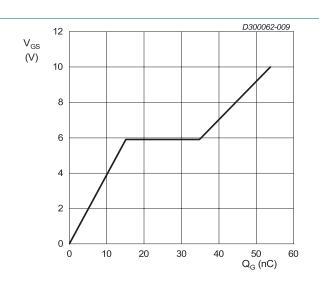
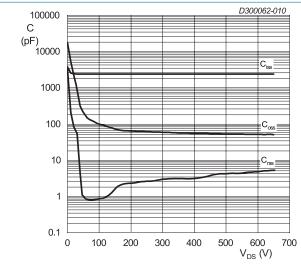


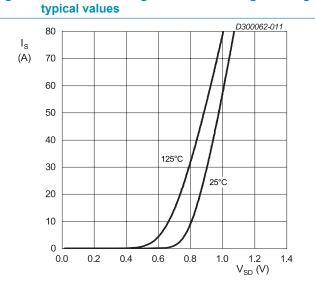
Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature



 $I_D = 15 \text{ A}$ ;  $V_{DS} = 400 \text{ V}$ Fig. 9. Gate-source voltage as a function of gate charge;



V<sub>GS</sub> = 0 V; f = 1 MHz Fig 10. Capacitances as a function of drain-source voltage; typical values



V<sub>GS</sub> = 0 V Fig 11. Source current as a function of source-drain voltage; typical values

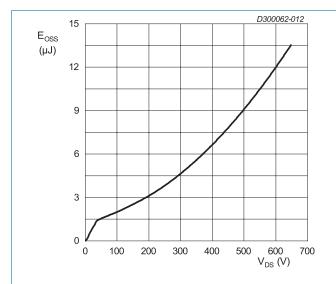
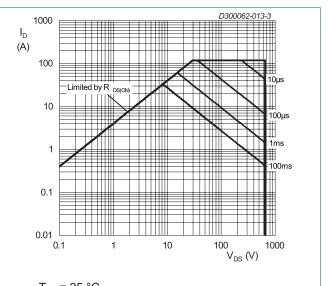
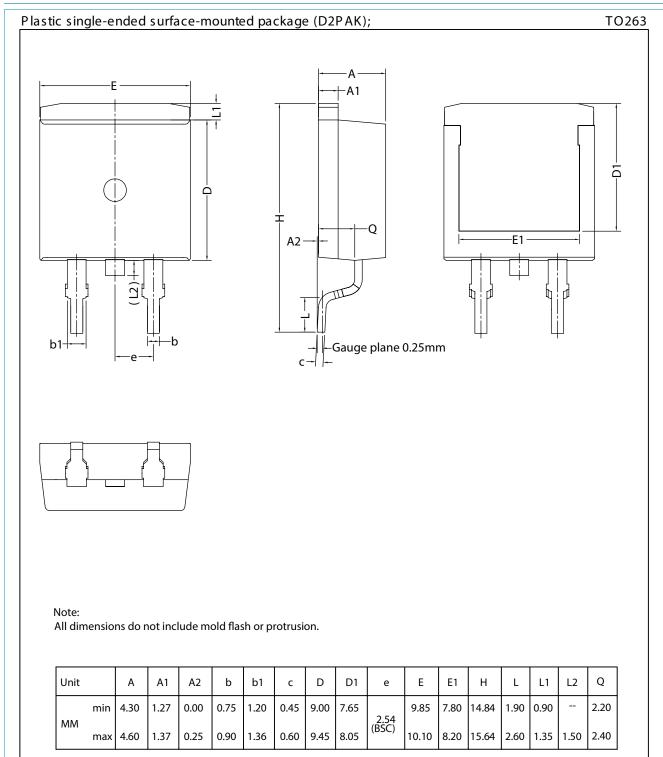


Fig. 12. Output capacitance stored energy as a function of drain-source voltage



 $T_{mb}$  = 25 °C Fig. 13. Safe operating area

# 11. Package outline



### 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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