WNSC6D50650W

Silicon Carbide Diode

Rev.01 - 03 September 2024 Product data sheet

1. General description

Dual Silicon Carbide Schottky diode in a TO247-2L plastic package, designed for high frequency switching mode power supplies.

RoHS Halogen-Free Lead-Free

2. Features and benefits

- New 6th generation technology
- Low forward voltage drop
- Low reverse leakage current
- High forward surge capability I_{FSM}
- Reduced losses in associated MOSFET
- Reduced EMI
- Reduced cooling requirements
- RoHS compliant

3. Applications

- PC/Telecom/Server SMPS
- UPS & energy storage systems
- Battery formation systems
- EV chargers
- PV inverter and MPPT circuit
- Motor Drives

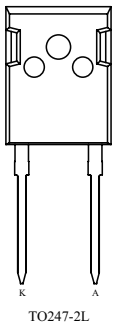
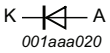
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V_{RRM}	repetitive peak reverse voltage			650			V
I_F	continuous forward current	T_{mb} ≤ 130 °C, DC; Fig. 2		50			A
T_j	junction temperature			-55 to 175			°C
Symbol	**Parameter**	**Conditions**	**Notes**	**Min**	**Typ**	**Max**	**Unit**
Static characteristics							
V_F	forward voltage	I_F = 50 A; T_j = 25 °C; Fig. 5		-	1.38	1.55	V
		I_F = 50 A; T_j = 150 °C; Fig. 5		-	1.60	1.80	V
Dynamic characteristics							
Q_r	recovered charge	I_F = 50 A; dI_F/dt = 500 A/µs; V_R = 400 V; T_j = 25 °C; Fig. 7		-	84	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 TO247-2L	 001aaa020
2	A	anode		
mb	mb	mounting base; connected to cathode		

6. Ordering information

Table 3. Ordering information

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC6D50650W	TO247-2L	WNSC6D50650W6Q	Tube	30	TO247P-2L	09-Mar-2023

7. Marking

Table 4. Marking codes

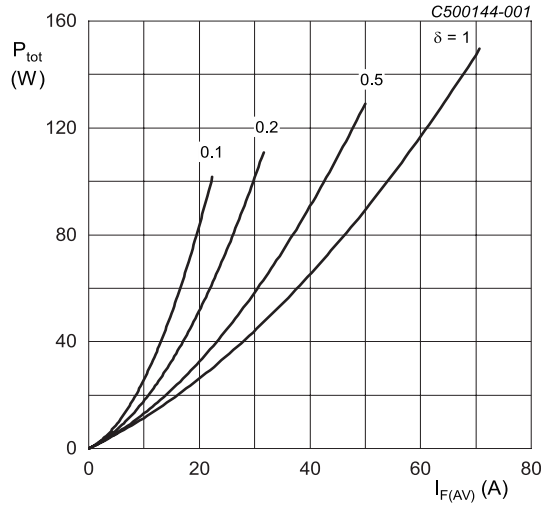
Type number	Marking codes
WNSC6D50650W	WNSC6D 50650W

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V_{RRM}	repetitive peak reverse voltage			650	V
V_{RWM}	crest working reverse voltage			650	V
V_R	reverse voltage	DC		650	V
I_F	continuous forward current	$T_{mb} \leq 130$ °C, DC; Fig. 2		50	A
		$T_{mb} \leq 125$ °C, DC; Fig. 2		54	A
		$T_{mb} \leq 25$ °C, DC; Fig. 2		110	A
I_{FRM}	repetitive peak forward current	$\delta = 0.5$; $t_p = 25$ µs; $T_{mb} \leq 125$ °C; square-wave pulse		85	A
I_{FSM}	non-repetitive peak forward current	$t_p = 10$ ms; $T_{j(init)} = 25$ °C; sine-wave pulse		320	A
		$t_p = 10$ µs; $T_{j(init)} = 25$ °C; square-wave pulse		1880	A
I^2t	I^2t for fusing	sine-wave pulse; $T_{j(init)} = 25$ °C; $t_p = 10$ ms		512	A^2s
T_{stg}	storage temperature			-55 to 175	°C
T_j	junction temperature			-55 to 175	°C



$I_{F(AV)} = I_{F(RMS)} \times \sqrt{\delta}$

$V_o = 0.992$ V; $R_s = 0.0159$ Ω

Fig. 1. Forward power dissipation as a function of average forward current; square waveform; maximum values

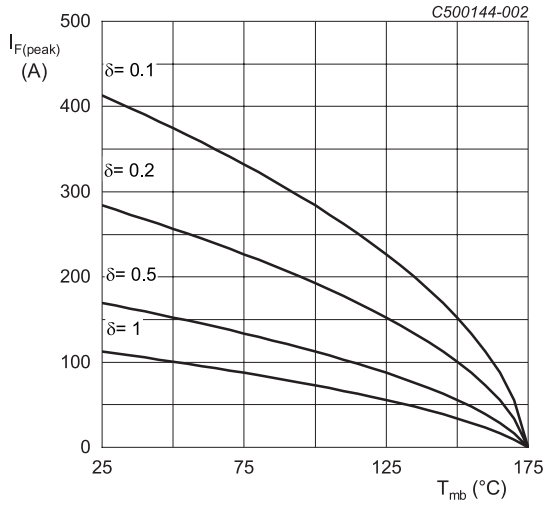


Fig. 2. Current derating as a function of mounting base temperature

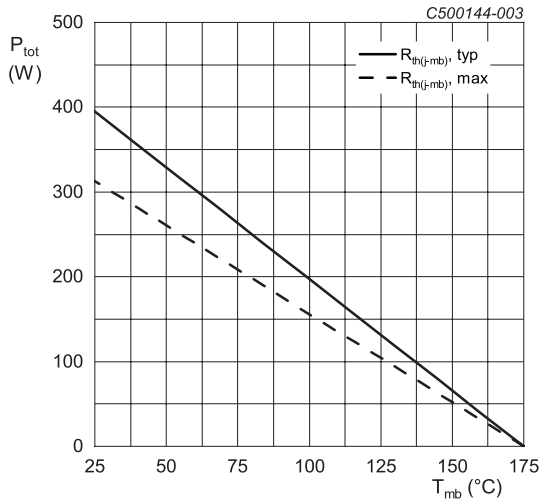


Fig. 3. Total power dissipation as a function of mounting base temperature

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 4		-	0.38	0.48	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	in free air		-	60	-	K/W

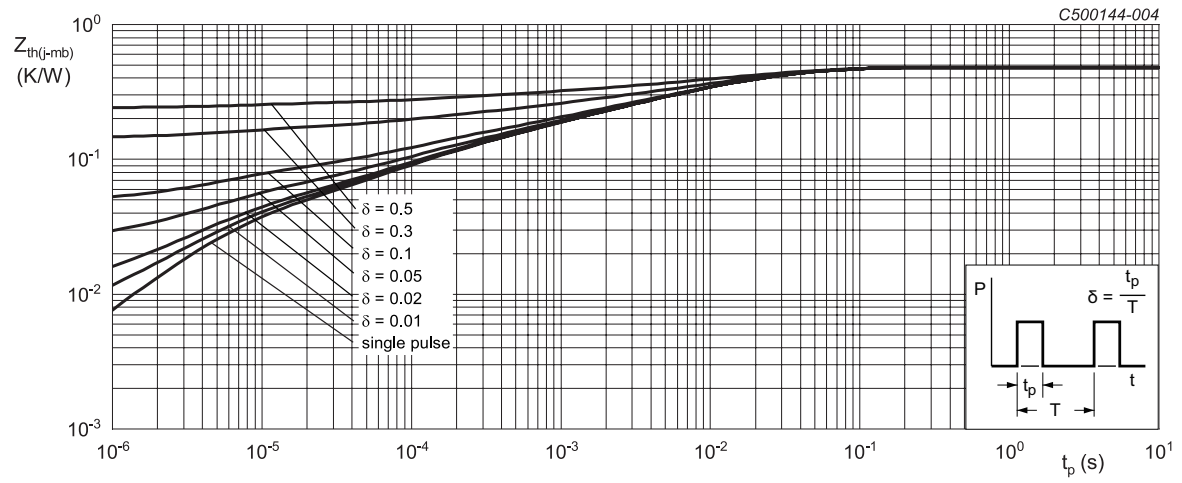
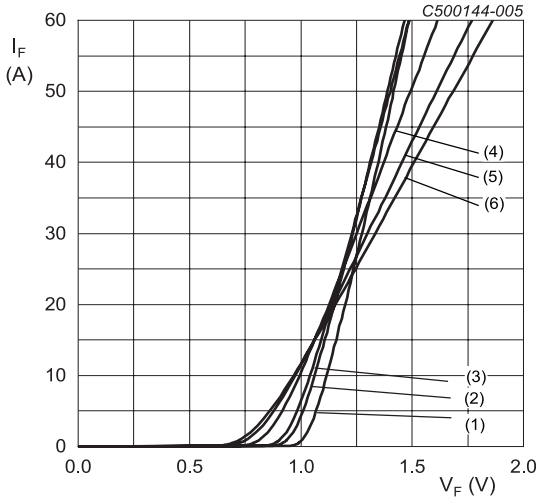


Fig. 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
V_F	forward voltage	I_F = 50 A; T_j = 25 °C; Fig. 5		-	1.38	1.55	V
		I_F = 50 A; T_j = 150 °C; Fig. 5		-	1.60	1.80	V
		I_F = 50 A; T_j = 175 °C; Fig. 5		-	1.70	1.90	V
I_R	reverse current	V_R = 650 V; T_j = 25 °C; Fig. 6		-	5	200	µA
		V_R = 650 V; T_j = 175 °C; Fig. 6		-	50	800	µA
Dynamic characteristics							
Q_r	recovered charge	I_F = 50 A; V_R = 400 V; dI_F/dt = 500 A/µs; T_j = 25 °C; Fig. 7		-	84	-	nC
C_d	diode capacitance	f = 1 MHz; V_R = 1 V; T_j = 25 °C		-	1900	-	pF
		f = 1 MHz; V_R = 300 V; T_j = 25 °C		-	226	-	pF
		f = 1 MHz; V_R = 600 V; T_j = 25 °C		-	214	-	pF
E_{as}	non-repetitive avalanche energy	I_R = 10 A; L = 5 mH; $T_{j(init)}$ = 25 °C		250	-	-	mJ



V_o = 0.992 V; R_s = 0.0159 Ω
(1) T_j = -55 °C; typical values
(2) T_j = 0 °C; typical values
(3) T_j = 25 °C; typical values
(4) T_j = 100 °C; typical values
(5) T_j = 150 °C; typical values
(6) T_j = 175 °C; typical values

Fig. 5. Forward current as a function of forward voltage; typical values

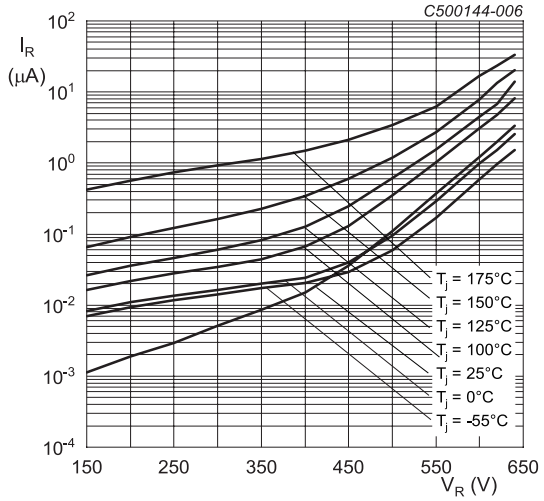


Fig. 6. Reverse leakage current as a function of reverse voltage; typical value

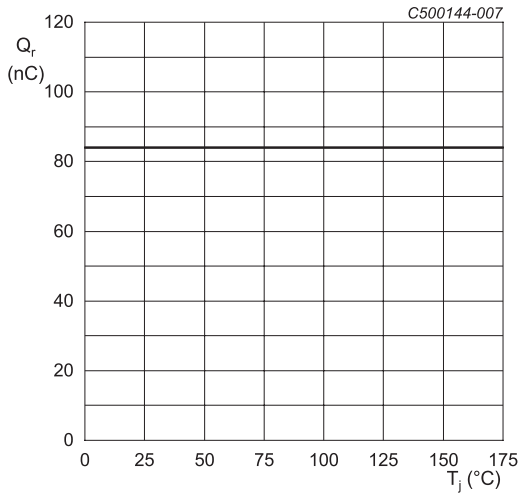
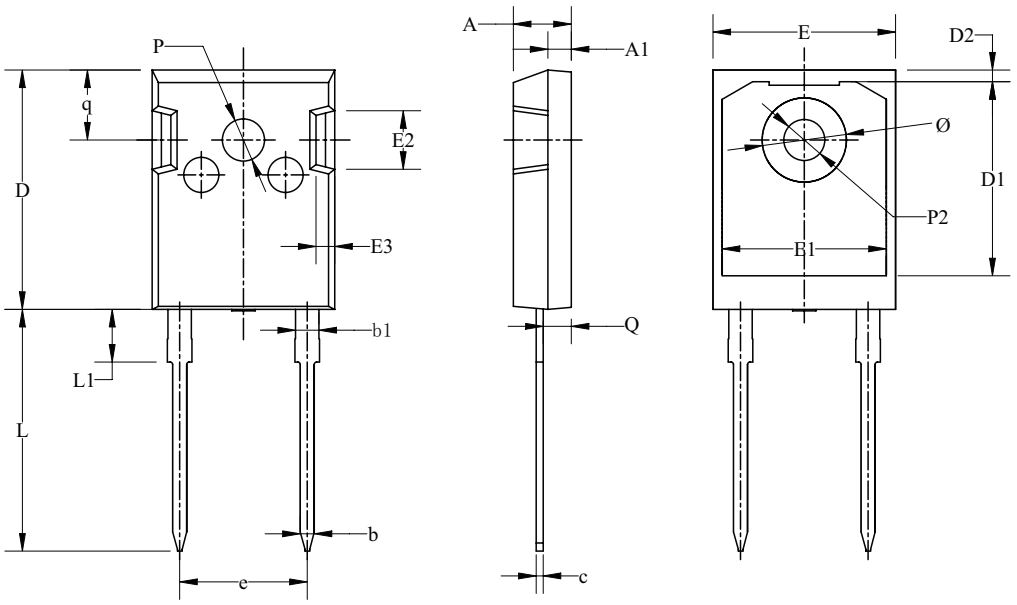


Fig. 7. Recovered charge as a function of junction temperature

11. Package outline

Plastic single-ended through-hole package; headsink mounted; 1 mounting hole; 2 leads TO-247 TO247-2L



Dim	All Dimensions in Millimeters		
	Min	Typ	Max
A	4.70	4.95	5.20
A1	1.90	2.00	2.10
b	1.00	1.20	1.40
b1	1.80	2.00	2.20
c	0.50	0.60	0.70
D	20.30	20.45	20.60
D1	16.20	16.58	16.87
D2	0.80	1.00	1.20
E	15.45	15.60	15.75
E1	13.82	14.02	14.22
E2	4.80	5.00	5.20
E3	1.40	1.60	1.80
e	10.90 BSC		
L	20.40	20.65	20.90
L1	4.25	4.50	4.75
P2	3.40	3.50	3.60
P	3.50	3.60	3.70
Q	2.20	2.40	2.60
q	5.78	5.98	6.18
Ø	7.10	7.19	7.30

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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