

## 1. General description

Planar passivated Silicon Controlled Rectifier (SCR) module in WeEnPACK-20mm (TO-240AA) for use in applications requiring high blocking voltage capability, high inrush current capability and high thermal cycling performance

## 2. Features and benefits

- High blocking voltage capability
- High thermal cycling performance
- Planar passivated for voltage ruggedness and reliability
- Package meets UL certification
- Package is RoHS compliant
- Industry standard outline
- Soldering pins for PCB mounting
- Copper base plate
- Cathode Kelvin contacts provided
- UL1557 certified (Document number E346397)

## 3. Applications

- Softstart AC motor control
- DC Motor control
- AC power control
- Power converter
- Temperature control
- Lighting control

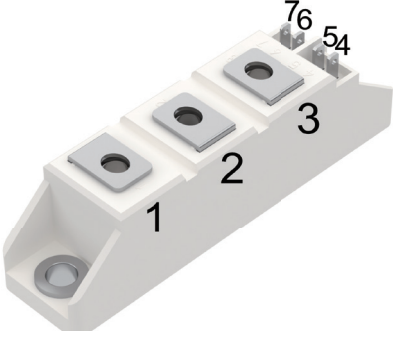
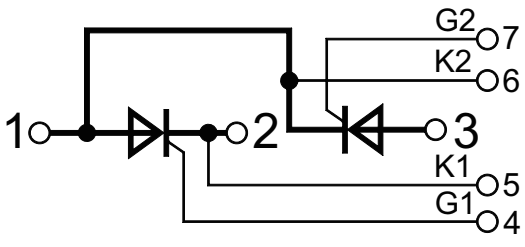
## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
<b>Absolute maximum rating</b>							
$V_{DRM}$	repetitive peak forward voltage			1200			V
$V_{RRM}$	repetitive peak reverse voltage			1200			V
$I_{T(RMS)}$	RMS on-state current	half sine wave		125			A
$I_{TSM}$	non-repetitive peak on-state current	half sine wave; $T_{j(init)} = 25\text{ °C}$ ; $t_p = 10\text{ ms}$		1700			A
		half sine wave; $T_{j(init)} = 150\text{ °C}$ ; $t_p = 10\text{ ms}$		1400			A
		half sine wave; $T_{j(init)} = 25\text{ °C}$ ; $t_p = 8.3\text{ ms}$		1850			A
		half sine wave; $T_{j(init)} = 150\text{ °C}$ ; $t_p = 8.3\text{ ms}$		1500			A
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
<b>Static characteristics</b>							
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 25\text{ °C}$		30	-	100	mA
$V_{GT}$	gate trigger voltage	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 25\text{ °C}$		-	0.70	1.20	V
$V_T$	on-state voltage	$I_T = 80\text{ A}$ ; $T_j = 25\text{ °C}$		-	-	1.29	V

## 5. Pinning information

Table 2. Pinning information

Simplified outline	Graphic symbol
	

## 6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WTMH80T12R	WeEnPACK-20mm	WTMH80T12RT	Tray	12	WeEnPACK-20mmPHB-C	26-Jun-2023

## 7. Marking

Table 4. Marking codes

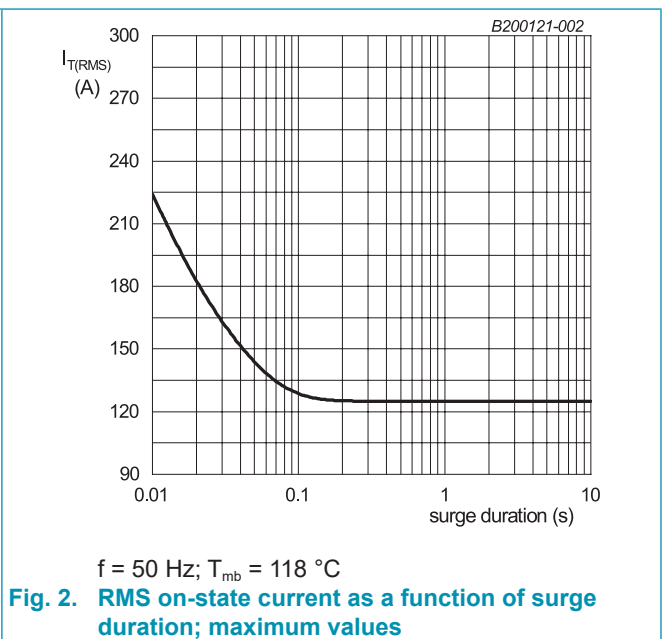
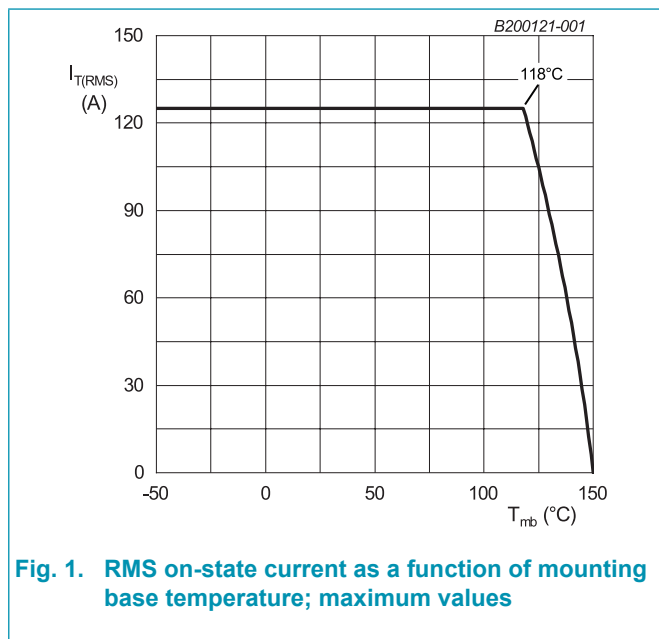
Type number	Marking codes
WTMH80T12R	WTMH80T12R

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
$V_{DRM}$	repetitive peak forward voltage			1200	V
$V_{RRM}$	repetitive peak reverse voltage			1200	V
$I_{T(AV)}$	average on-state current	half sine wave		80	A
$I_{T(RMS)}$	RMS on-state current	half sine wave		125	A
$I_{TSM}$	non-repetitive peak onstate current	half sine wave; $T_{j(Init)} = 25\text{ °C}$ ; $t_p = 10\text{ ms}$		1700	A
		half sine wave; $T_{j(Init)} = 150\text{ °C}$ ; $t_p = 10\text{ ms}$		1400	A
		half sine wave; $T_{j(Init)} = 25\text{ °C}$ ; $t_p = 8.3\text{ ms}$		1850	A
		half sine wave; $T_{j(Init)} = 150\text{ °C}$ ; $t_p = 8.3\text{ ms}$		1500	A
$I^2t$	$I^2t$ for fusing	$t_p = 10\text{ ms}$ ; sine-wave pulse		12.8	kA <sup>2</sup> s
$di_T/dt$	rate of rise of on-state current	$I_G = 200\text{ mA}$ ; $T_j = 150\text{ °C}$		200	A/ $\mu$ s
$I_{GM}$	peak gate current			10	A
$V_{RGM}$	peak reverse gate voltage			5	V
$P_{GM}$	peak gate power			20	W
$P_{G(AV)}$	average gate power	over any 20 ms period		0.5	W
$T_{vj}$	virtual junction temperature			-40 to 150	°C
$T_{op}$	operation temperature			-40 to 130	°C
$T_{stg}$	storage temperature			-40 to 130	°C



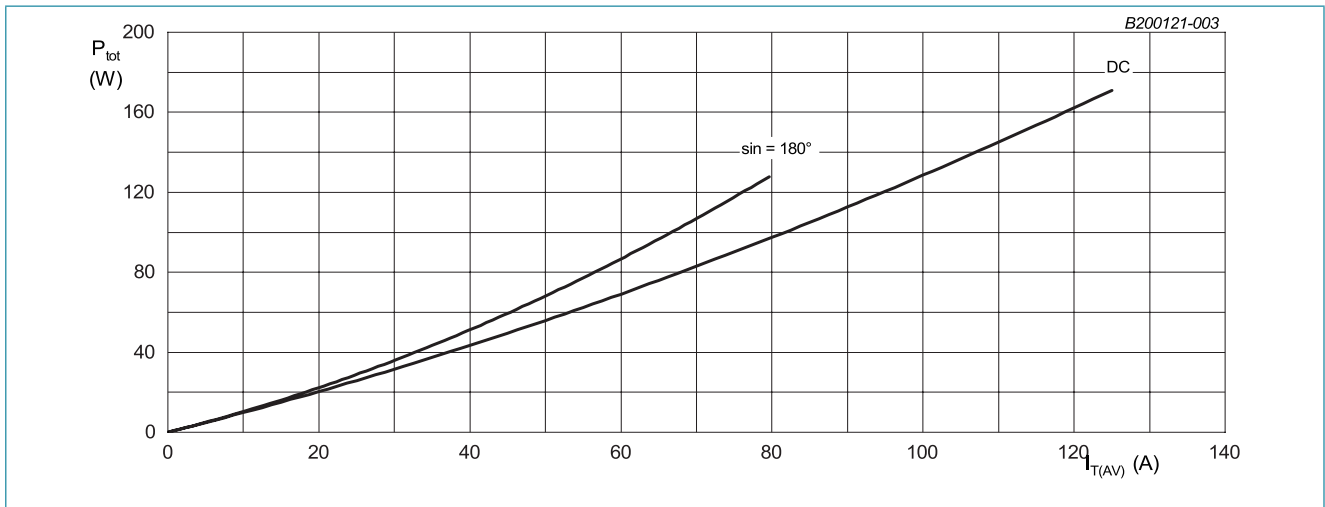


Fig. 3. Total power dissipation as a function of average on-state current; maximum values; per thyristor

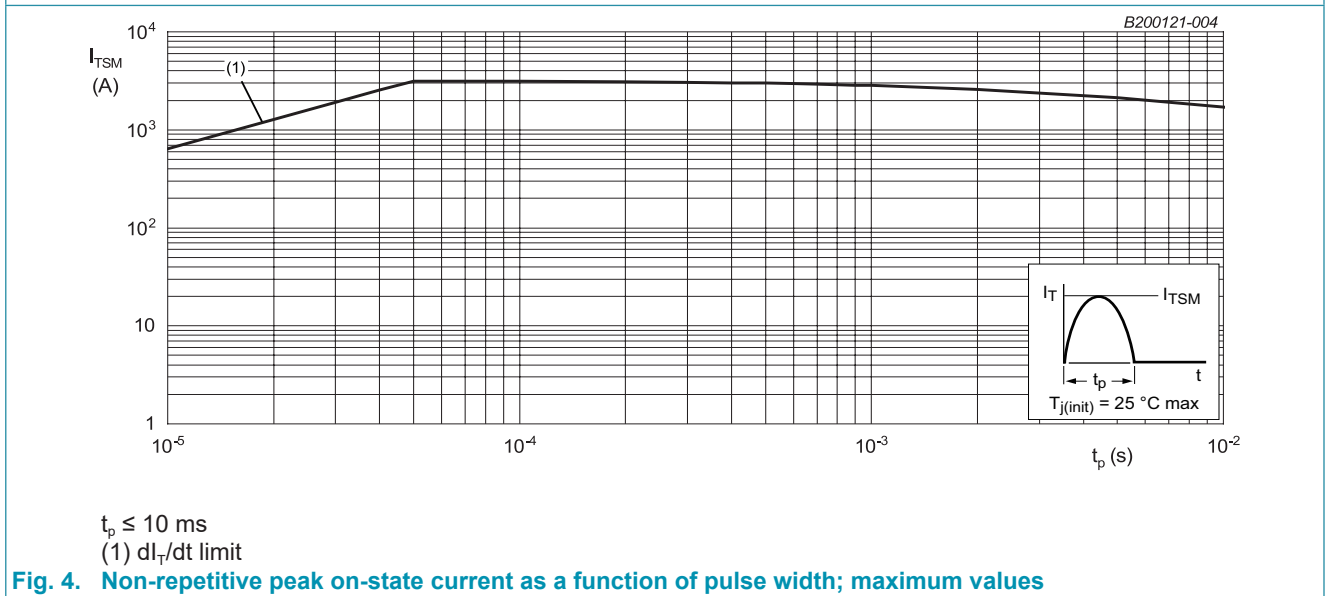


Fig. 4. Non-repetitive peak on-state current as a function of pulse width; maximum values

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	per thyristor		-	-	0.25	K/W
		per module		-	-	0.125	K/W
$R_{th(j-h)}$	thermal resistance from junction to heatsink	per thyristor		-	-	0.48	K/W
		per module		-	-	0.24	K/W

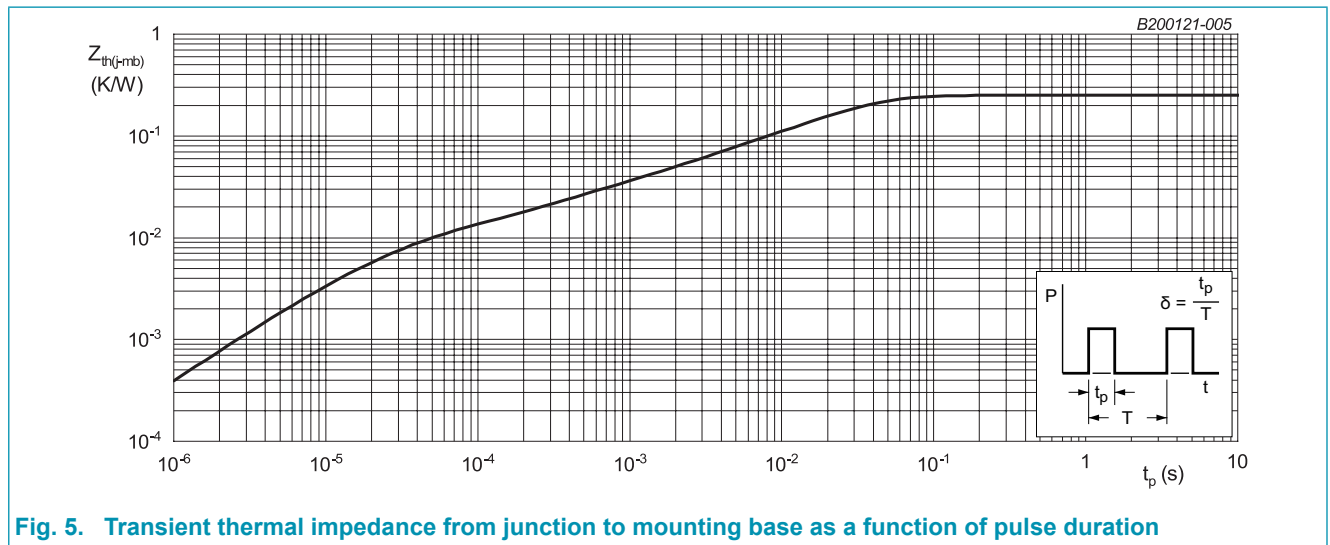


Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

## 10. Package characteristics

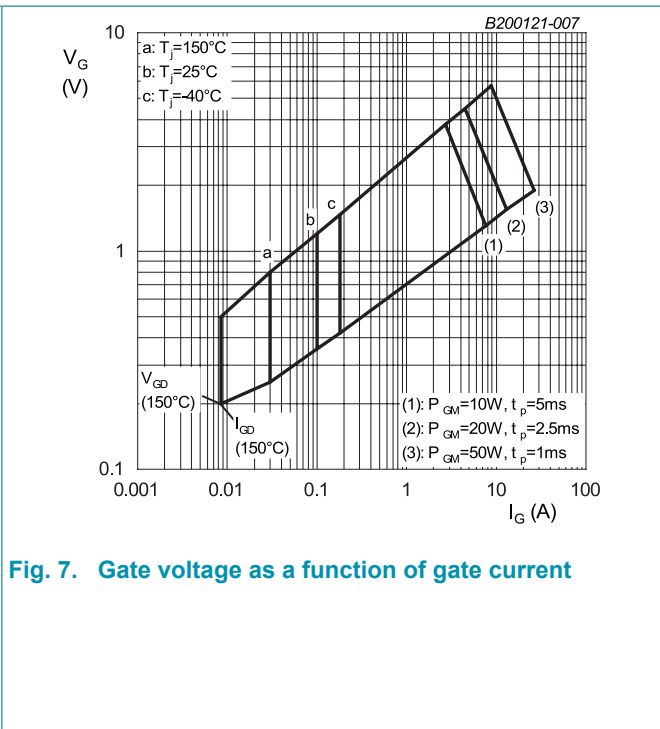
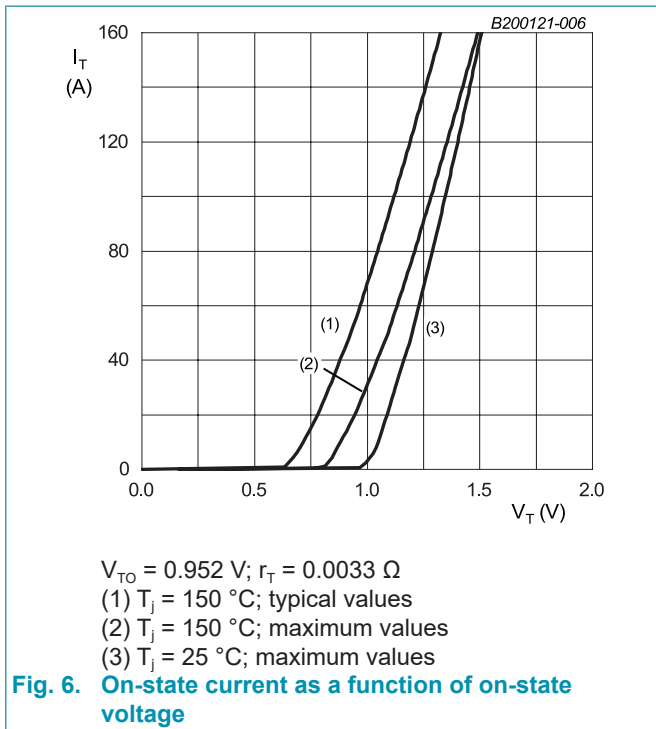
Table 7. Isolation characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$V_{isol}$	isolation voltage	50/60 Hz; RMS; $I_{ISOL} \leq 1 \text{ mA}$ ; $t = 1 \text{ second}$ ; AC		-	-	3600	V
		50/60 Hz; RMS; $I_{ISOL} \leq 1 \text{ mA}$ ; $t = 1 \text{ minute}$ ; AC		-	-	3000	V

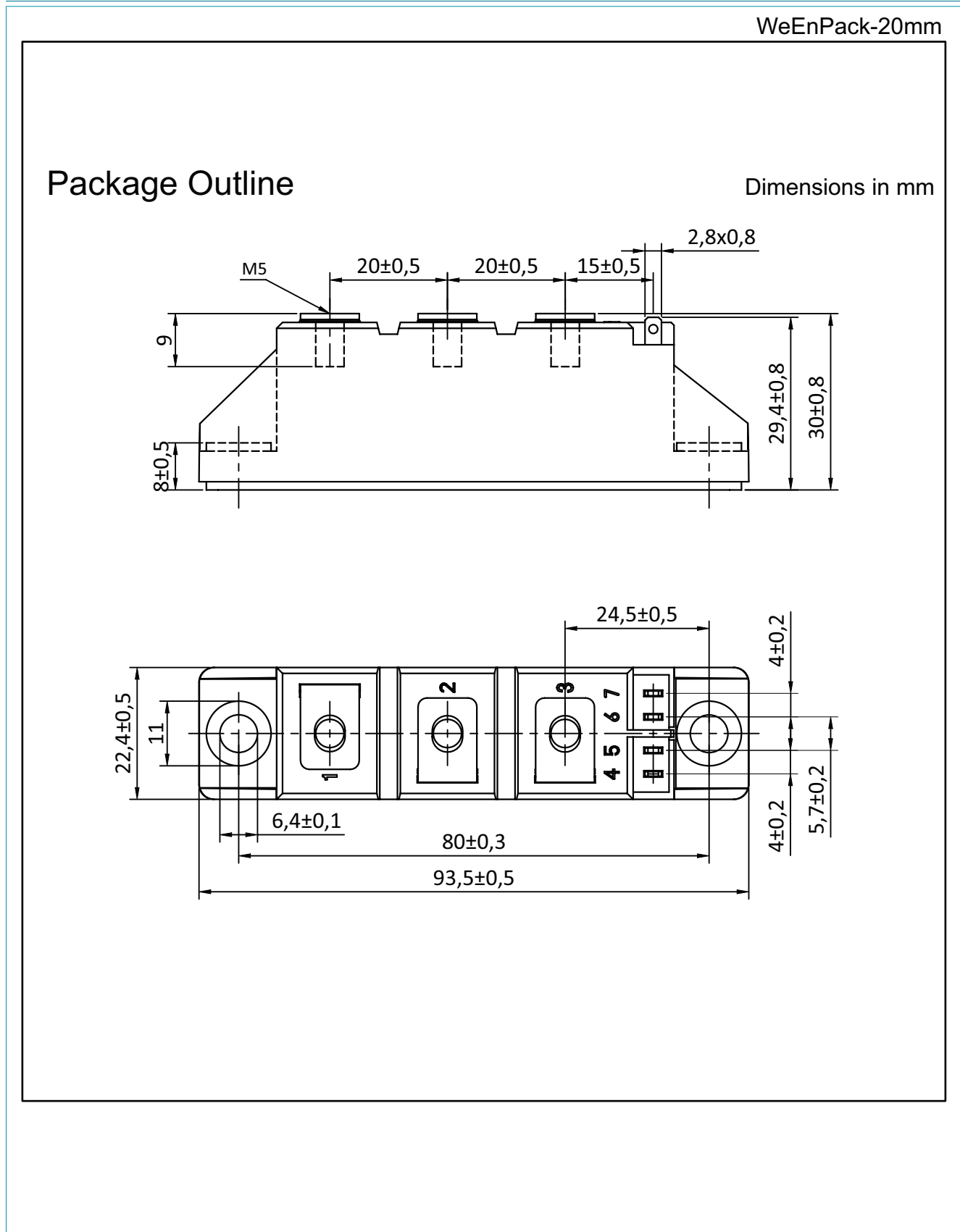
## 11. Characteristics

Table 8. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
<b>Static characteristics</b>							
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_j = 25\text{ }^\circ\text{C}$		30	-	100	mA
$V_{GT}$	gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_j = 25\text{ }^\circ\text{C}$		-	0.70	1.20	V
		$V_D = 2/3 V_{DRM}; I_T = 0.1\text{ A}; T_j = 150\text{ }^\circ\text{C}$		0.25	0.4	-	V
$I_{GD}$	gate non-trigger current	$T_j = 150\text{ }^\circ\text{C}$		-	-	8.5	mA
$V_{GD}$	gate non-trigger voltage	$T_j = 150\text{ }^\circ\text{C}$		-	-	0.2	V
$I_L$	latching current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_j = 25\text{ }^\circ\text{C}$		-	-	300	mA
$I_H$	holding current	$V_D = 12\text{ V}; T_j = 25\text{ }^\circ\text{C}$		-	-	200	mA
$V_T$	on-state voltage	$I_T = 80\text{ A}; T_j = 25\text{ }^\circ\text{C}$		-	-	1.29	V
		$I_T = 160\text{ A}; T_j = 25\text{ }^\circ\text{C}$		-	-	1.51	V
$V_{TO}$	threshold voltage	$T_j = 150\text{ }^\circ\text{C}$		-	-	0.95	V
$r_T$	slope resistance	$T_j = 150\text{ }^\circ\text{C}$		-	-	3.3	m $\Omega$
$I_D$	off-state current	$V_D = 1200\text{ V}; T_j = 25\text{ }^\circ\text{C}$		-	-	100	$\mu\text{A}$
		$V_D = 1200\text{ V}; T_j = 150\text{ }^\circ\text{C}$		-	-	10	mA
$I_R$	reverse current	$V_R = 1200\text{ V}; T_j = 25\text{ }^\circ\text{C}$		-	-	100	$\mu\text{A}$
		$V_R = 1200\text{ V}; T_j = 150\text{ }^\circ\text{C}$		-	-	10	mA
<b>Dynamic characteristics</b>							
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 804\text{ V}; T_j = 150\text{ }^\circ\text{C}; (V_{DM} = 67\% \text{ of } V_{DRM}); \text{exponential waveform}; \text{gate open circuit}$		1500	-	-	V/ $\mu\text{s}$
$t_{gt}$	gate-controlled turn-on time	$I_{TM} = 40\text{ A}; V_D = 800\text{ V}; I_G = 100\text{ mA}; (dI_G/dt)_M = 1\text{ A}/\mu\text{s}; T_j = 25\text{ }^\circ\text{C}$		-	2	-	$\mu\text{s}$
$t_q$	commutated turn-off time	$I_{TM} = 2\text{ A}; t_p = 50\text{ } \mu\text{s}; dV/dt = 5\text{ V}/\mu\text{s}; dI/dt = 30\text{ A}/\mu\text{s}; T_j = 25\text{ }^\circ\text{C}$		-	150	-	$\mu\text{s}$



12. Package outline





## 13. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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