**Product data sheet** 

## 1. General description

WG30N65HF1 uses advanced Fine Trench Field-stop IGBT technology with anti-parallel diode in TO-220AB package to provide extremely low  $V_{\text{CE(sat)}}$ , and excellent switching performance. This device offers Best-in-Class efficiency in hard switching and resonant topology.





## 2. Features and benefits

- · Maximum junction temperature 175 °C
- · Positive Temperature efficient for easy paralleling
- · Very soft, fast recovery anti-parallel diode
- · High switching speed
- · EMI Improved Design

## 3. Applications

- PFC
- Solar converters
- UPS
- Welding Converters
- · Mid to high range switching frequency converters

## 4. Quick reference data

### Table 1. Quick reference data

Symbol	Parameter	Parameter						
V <sub>CE</sub>	Collector-emitter voltage, T <sub>j</sub> ≥			30 A		V		
I <sub>C</sub>	DC collector current, limited by $T_C = 100  ^{\circ}C$			30		Α		
Symbol	Parameter Conditions			Min	Тур	Max	Unit	
Static cha	Static characteristics							
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}; I_C = 30 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	1.55	2.1	V	

# 5. Pinning information

## **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	•C
2	С	collector		
3	Е	emitter		
mb	С	mounting base; connected to collector		G E sym200

# 6. Ordering information

## **Table 3. Ordering information**

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WG30N65HF1	TO-220AB	WG30N65HF1Q	Tube	50	SOT78	13-Jun-2008

## 7. Marking

## **Table 4. Marking codes**

Type number	Marking codes
WG30N65HF1	G30N65 HF1

# 8. Limiting values

## Table 5. Limiting values

Symbol	Parameter	Notes	Value	Unit
V <sub>CE</sub>	Collector-emitter voltage, T <sub>j</sub> ≥ 25 °C		650	V
I <sub>C</sub>	DC collector current, limited by $T_{j(max)}$ $T_{c}$ = 25 °C $T_{c}$ = 100 °C		60 30	А
I <sub>C(puls)</sub>	Pulsed collector current, t <sub>p</sub> limited by T <sub>j(max)</sub>		90	А
-	Turn off safe operating area $V_{CE} \le 650 \text{ V}, T_j \le 175 ^{\circ}\text{C}, t_p = 1 \mu\text{s}$		90	А
l <sub>F</sub>	Diode forward current, limited by $T_{j(max)}$ $T_{c}$ = 25 °C $T_{c}$ = 100 °C		60 30	А
I <sub>Fpuls</sub>	Diode pulsed current, t <sub>p</sub> limited by T <sub>j(max)</sub>		90	Α
$V_{GE}$	Gate-emitter voltage		±20	V
P <sub>tot</sub>	Power dissipation $T_c$ = 25 °C Power dissipation $T_c$ = 100 °C		312 156	W
T <sub>stg</sub>	Storage temperature		-55 to +150	°C
$T_{jmax}$	Maximum operating junction temperature		175	°C
-	Peak soldering temperture		260	°C
M	Mounting Torque with washer		0.55	Nm

## 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-c)</sub>	IGBT thermal resistance from junction to case			-	0.48	-	K/W
R <sub>th(j-c)</sub>	Diode thermal resistance from junction to case			-	0.94	-	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient			-	40	-	K/W

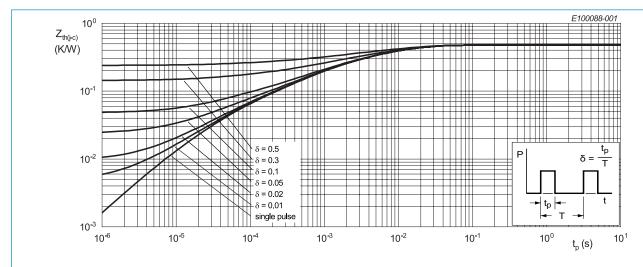


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; IGBT

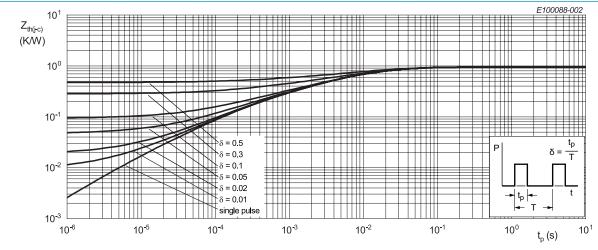


Fig. 2. Transient thermal impedance from junction to case as a function of pulse duration; Diode

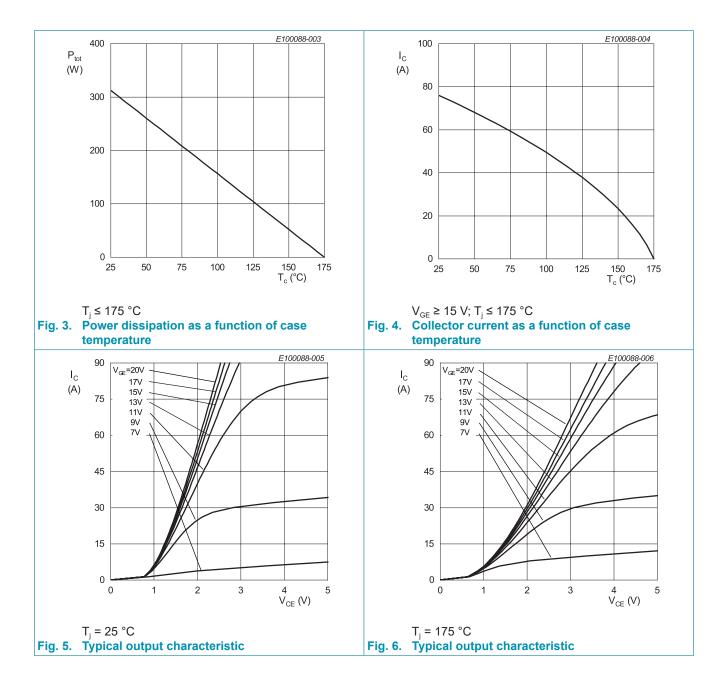
## 10. Characteristics

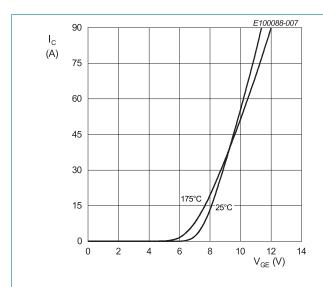
### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$BV_CES$	Collector-emitter breakdown voltage	$V_{GE} = 0 \text{ V}; I_{C} = 1.0 \text{ mA}$		650	-	-	V
$V_{\text{CE(sat)}}$	Collector-emitter saturation	$V_{GE} = 15 \text{ V}; I_{C} = 30 \text{ A}; T_{j} = 25 \text{ °C}$		-	1.55	2.1	V
	voltage	$V_{GE}$ = 15 V; $I_{C}$ = 30 A; $T_{j}$ = 175 °C		-	2.05	-	V
V <sub>F</sub>	Diode forward voltage	$V_{GE} = 0 \text{ V}; I_F = 30 \text{ A}; T_j = 25 \text{ °C}$		-	1.75	-	V
		V <sub>GE</sub> = 0 V; I <sub>F</sub> = 30 A; T <sub>j</sub> = 175 °C		-	1.45	-	V
$V_{\text{GE(th)}}$	Gate-emitter threhold voltage	$I_{\rm C}$ = 0.6 mA; $V_{\rm CE}$ = $V_{\rm GE}$		4.3	5.4	6.5	V
I <sub>CES</sub>	Zero gate voltage collector	$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	-	100	μA
	current	$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 175 ^{\circ}\text{C}$		-	-	1	mA
g <sub>fs</sub>	Transconductance	V <sub>CE</sub> = 20 V; I <sub>C</sub> = 30 A		-	21	-	S
Dynamic	characteristics						
C <sub>ies</sub>	Input capacitance	$V_{CE} = 30 \text{ V}; V_{GE} = 0 \text{ V}; f = 1 \text{ MHz};$		-	1638	-	pF
C <sub>oes</sub>	Output capacitance	T <sub>j</sub> = 25 °C		-	65	-	pF
C <sub>res</sub>	Reverse transfer capacitance			-	19	-	pF
$Q_{G}$	Gate charge	$V_{CC}$ = 520 V; $I_{C}$ = 30 A; $V_{GE}$ = 15 V; $T_{i}$ = 25 °C		-	74	-	nC

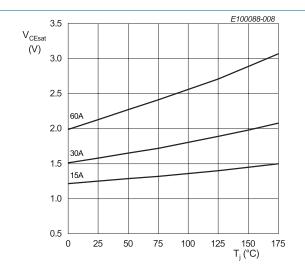
# 11. Switching Characteristics

	witching Characteristics, Ir Parameter	Conditions	Notes	Min	Trun	Max	Unit
Symbol		Conditions	Notes	IVIIII	Тур	IVIAX	Unit
	racteristics						
t <sub>d(on)</sub>	Turn-on delay time	$T_j = 25 ^{\circ}\text{C};$ $V_{CC} = 400 \text{V};  I_C = 30 \text{A};  V_{GE} = 15 \text{V} / 0 \text{V};$		-	33.8	-	nS
$t_r$	Rise time	$R_{\rm G} = 10  \Omega$		-	37.4	-	nS
$t_{\text{d(off)}} \\$	Turn-off delay time			-	129	-	nS
t <sub>f</sub>	Fall time			-	24	-	nS
E <sub>on</sub>	Turn-on energy			-	0.8	-	mJ
E <sub>off</sub>	Turn-off energy			-	0.3	-	mJ
E <sub>ts</sub>	Total switching energy			-	1.1	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	T <sub>j</sub> = 175 °C;		-	32	-	nS
t <sub>r</sub>	Rise time	$V_{CC} = 400 \text{ V}; I_C = 30 \text{ A}; V_{GE} = 15 \text{V} / 0 \text{V};$ $R_G = 10 \Omega$		-	34.4	-	nS
$t_{d(off)}$	Turn-off delay time			-	154	-	nS
t <sub>f</sub>	Fall time			-	37	-	nS
E <sub>on</sub>	Turn-on energy			-	1.3	-	mJ
E <sub>off</sub>	Turn-off energy			-	0.45	-	mJ
E <sub>ts</sub>	Total switching energy			-	1.75	-	mJ
Diode cha	aracteristics						
t <sub>rr</sub>	Reverse recovery time	T <sub>j</sub> = 25 °C;		-	48.5	-	nS
Q <sub>r</sub>	Reverse recovery charge	$V_R = 400 \text{ V}; I_F = 30 \text{ A}; dI_F/dt = 500 \text{A/us}$		-	336	-	nC
I <sub>RM</sub>	Reverse recovery peak current			-	12.5	-	А
t <sub>rr</sub>	Reverse recovery time	T <sub>j</sub> = 175 °C;		-	101	-	nS
Q <sub>r</sub>	Reverse recovery charge	$V_R = 400 \text{ V}; I_F = 30 \text{ A}; dI_F/dt = 500 \text{A/us}$		-	1193	-	nC
I <sub>RM</sub>	Reverse recovery peak current			-	21	-	А

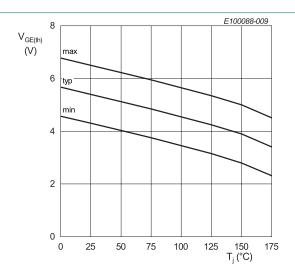




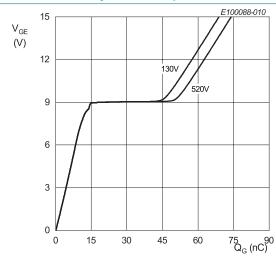
 $V_{CE}$  = 20 V Fig. 7. Typical transfer characteristic



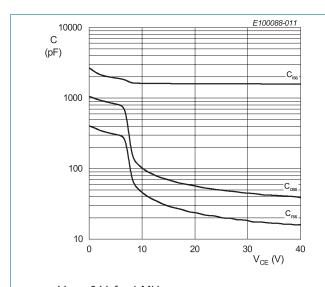
V<sub>GE</sub> = 15 V
Fig. 8. Typical collector-emitter saturation voltage as a function of junction temperature



 $I_c = 600~\mu A$  Fig. 9. Gate-emitter threshold voltage as a function of junction temperature



 $I_c = 30 \text{ A}$  Fig. 10. Typical gate charge



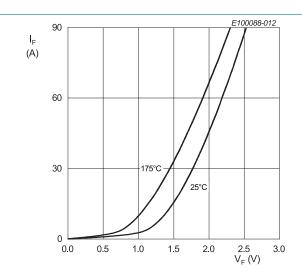
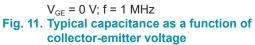


Fig. 12. Typical diode forward current as a function of forward voltage



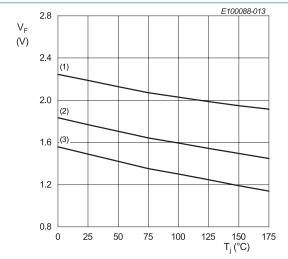
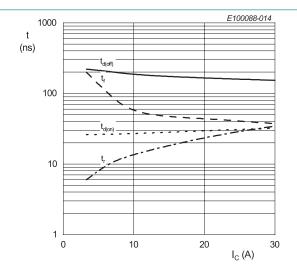


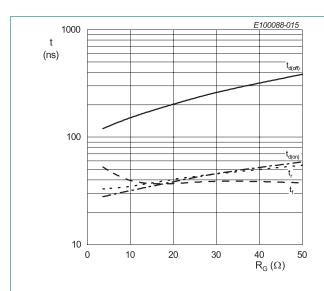


Fig. 13. Typical diode forward voltage as a function of junction temperature



 $R_{g}$  = 10  $\Omega;$   $V_{GE}$  = 15V/0V;  $T_{j}$  = 175 °C;  $V_{CE}$  = 400 V; inductive load

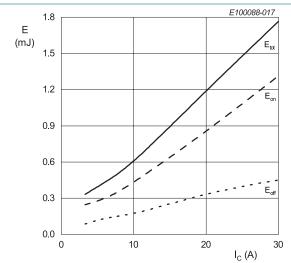
Fig. 14. Typical switching times as a function of collector current



 $I_C = 30 \text{ A}; V_{GE} = 15 \text{V/OV}; T_j = 175 ^{\circ}\text{C};$ 

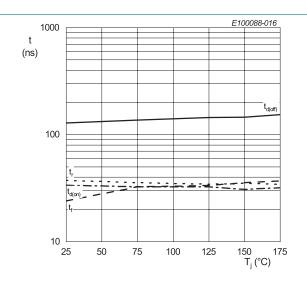
V<sub>CE</sub> = 400 V; inductive load

Fig. 15. Typical switching times as a function of gate resistance



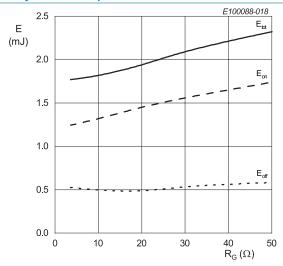
 $R_g$  = 10  $\Omega$ ;  $V_{GE}$  = 15V/0V;  $T_j$  = 175 °C;  $V_{CE}$  = 400 V; inductive load

Fig. 17. Typical switching energy losses as a function of collector current



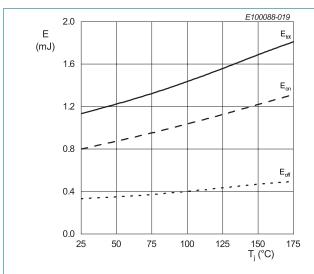
 $I_{C}$  = 30 A;  $V_{GE}$  = 15V/0V;  $R_{g}$  = 10  $\Omega$ ;  $V_{CE}$  = 400 V; inductive load

Fig. 16. Typical switching times as a function of junction temperature



 $I_{C}$  = 30 A;  $V_{GE}$  = 15V/0V;  $T_{j}$  = 175 °C;  $V_{CE}$  = 400 V; inductive load

Fig. 18. Typical switching energy losses as a function of gate resistance



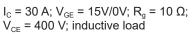


Fig. 20. Forward bias safe operating area



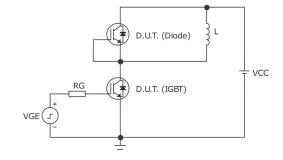


Fig. 21. Test circuit for inductive load switching

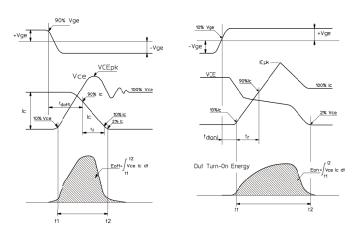
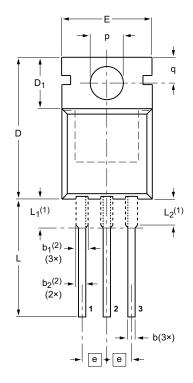


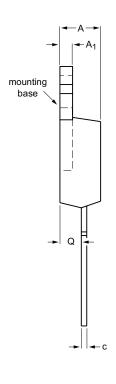
Fig. 22. Definition of switching times and losses

# 12. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78





0 5 10 mm

#### **DIMENSIONS** (mm are the original dimensions)

ι	JNIT	A	A <sub>1</sub>	b	b <sub>1</sub> <sup>(2)</sup>	b <sub>2</sub> (2)	C	D	D <sub>1</sub>	E	е	L	L <sub>1</sub> <sup>(1)</sup>	L <sub>2</sub> <sup>(1)</sup> max.	р	q	ø
	mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

- Notes
  1. Lead shoulder designs may vary.
- 2. Dimension includes excess dambar.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT78		3-lead TO-220AB	SC-46		<del>08-04-23</del> 08-06-13

## 13. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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For more information, please visit: http://www.ween-semi.com For sales office addresses, please send an email to: salesaddresses@ween-semi.com Date of release: 24 January 2024

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