# **WMSC008H12B2S**

## **N-Channel Silicon Carbide MOSFET Module**

Rev.02 - 24 September 2024

**Product data sheet** 

## 1. General description

WeEnPACK-B2 module with WeEn 1200V Gen2 SiC MOSFET and Solder pin type. Integrated with NTC temperature sensor.



## 2. Features and benefits

- · Half bridge topology
- Solder pin configuration
- Low R<sub>DSon</sub>-T<sub>j</sub> coefficient
- Low Switching Losses
- Low Q<sub>a</sub> and C<sub>rss</sub>
- Mimimized circuit impedance
- Improved chip synchronization performance

## 3. Applications

- Power inverters
- AC-DC converters
- · DC-DC converters
- · Active power factor correctors
- Motor drives

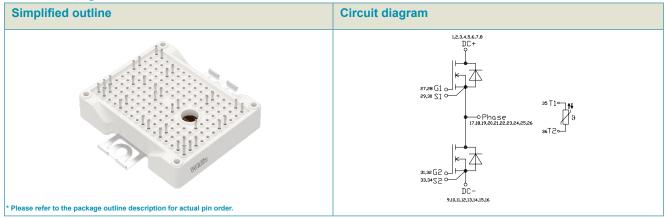
### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating						
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C			1200		V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>h</sub> = 25 °C			157		Α
P <sub>tot</sub>	total power dissipation	T <sub>h</sub> = 25 °C			272		W
$T_{j.op}$	operating junction temperature			-	-40 to 15	0	°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
$R_{\text{DS(on)}}$	drain-source on-state resistance	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 25 °C		-	8.0	-	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 25 °C		-	6.7	13	mΩ
Dynamic	characteristics						'
Q <sub>G(tot)</sub>	total gate charge	$I_D = 150 \text{ A}$ ; $V_{DS} = 800 \text{ V}$ ; $V_{GS} = 0 \text{ V}/18 \text{ V}$ ;		-	536	-	nC
$Q_{GD}$	gate-drain charge	T <sub>j</sub> = 25 °C		-	102	-	nC
Source-d	rain diode						
Q <sub>r</sub>	recovered charge	$I_{SD}$ = 150 A; $V_{GS}$ = -4 V/18 V; $V_{R}$ = 600 V; di/dt =2700 A/ $\mu$ s;		-	928	-	nC

## 5. Pinning information

#### **Table 2. Pinning information**



## 6. Ordering information

**Table 3. Ordering information** 

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	•	Package issue date
WMSC008H12B2S	WeEnPACK-B2	WMSC008H12B2S6T	Tray	12	WeEnPACK- B2PHB-A	31-Jan-2024

## 7. Marking

### Table 4. Marking codes

Type number	Marking codes
WMSC008H12B2S	WMSC008H12B2S

## 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Parameter	Conditions	Notes	Values	Unit
storage temperature			-40 to 125	°C
operating junction temperature			-40 to 150	°C
maximum junction temperature	Intermittent condition with shortened lifetime		-40 to 175	°C
RMS isolation voltage	T <sub>j</sub> = 25 °C; all terminals shorted; f = 50 Hz; t = 1 s		3500	V
drain-source voltage	T <sub>j</sub> = 25 °C		1200	V
gate-source voltage	Absolute maximum values		-12 to 24	V
gate-source voltage	Recommended operational values		-4 to 18	V
total power dissipation	T <sub>h</sub> = 25 °C		272	W
drain current	V <sub>GS</sub> = 18 V; T <sub>h</sub> = 25 °C		157	Α
	V <sub>GS</sub> = 18 V; T <sub>h</sub> = 100 °C		99	Α
peak drain current	pulsed; tp $\leq$ 10 us; T <sub>h</sub> = 25 °C		350	А
single pulse drain-to- source avalanche	$I_{AS} = 24 \text{ A}; L = 1 \text{ mH}; V_{DD} = 100 \text{ V};$ $T_{j(init)} = 25 \text{ °C}; \text{ each die}$		288	mJ
e				
DC body diode forward current	$T_h = 25  ^{\circ}C;  V_{GS} = -4  V$		65	Α
Pulse body diode current	verified by design, tp limited by $T_{jmax}$		350	Α
	storage temperature operating junction temperature maximum junction temperature RMS isolation voltage  drain-source voltage gate-source voltage gate-source voltage total power dissipation drain current  peak drain current single pulse drain-to-source avalanche  DC body diode forward current	storage temperature operating junction temperature maximum junction temperature Intermittent condition with shortened lifetime RMS isolation voltage $ T_{j} = 25  ^{\circ}\text{C}; \text{ all terminals shorted}; \\ f = 50  \text{Hz}; t = 1  \text{s} $	storage temperature operating junction temperature Intermittent condition with shortened lifetime RMS isolation voltage $T_j = 25  ^{\circ}\text{C}$ ; all terminals shorted; $f = 50  \text{Hz}$ ; $t = 1  \text{s}$ drain-source voltage $T_j = 25  ^{\circ}\text{C}$ gate-source voltage Absolute maximum values gate-source voltage Recommended operational values total power dissipation $T_h = 25  ^{\circ}\text{C}$ $V_{GS} = 18  \text{V}$ ; $T_h = 25  ^{\circ}\text{C}$ $V_{GS} = 18  \text{V}$ ; $T_h = 25  ^{\circ}\text{C}$ $V_{GS} = 18  \text{V}$ ; $V_{GS} = 18$	storage temperature

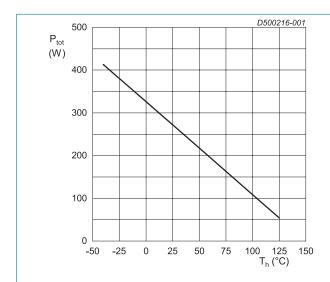


Fig. 1. Power dissipation as a function of heatsink temperature; maximum values

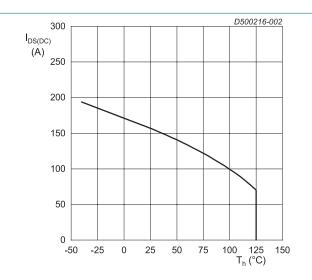


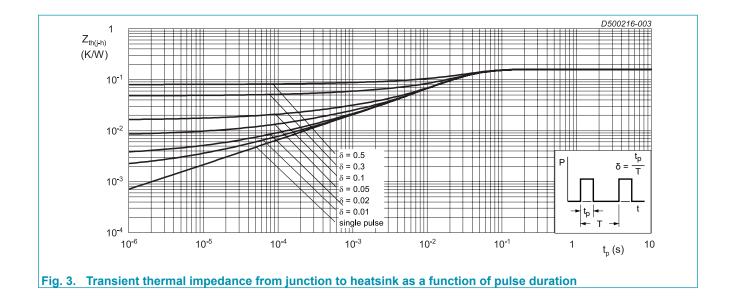
Fig. 2. Continuous Drain Current as a function of heatsink temperature

## 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{\text{th(j-c)}}$	thermal resistance from junction to case	per MOSFET		-	0.16	-	K/W
$R_{th(j-h)}$	thermal resistance from junction to heatsink	per MOSFET, $\lambda_{grease} = 3 \text{ W/(m·K)}$ , thick <sub>grease</sub> = 50 um		-	0.46	-	K/W
Internal Is	solation	basic insulation (class 1, IEC 61140)			$Al_2O_3$		
d <sub>Creep</sub>	Creepage distance	terminal to heatsink		-	11.5	-	mm
		terminal to terminal		-	6.3	-	mm
d <sub>Clear</sub>	Clearance	terminal to heatsink		-	10	-	mm
		terminal to terminal		-	5	-	mm
CTI	Comperative tracking index				>200		
F	Mounting force per clamp			40	-	80	N
G	Approximate Weight			-	36	-	g

Note: Module is ESD sensitive. Handling precautions are recommended.

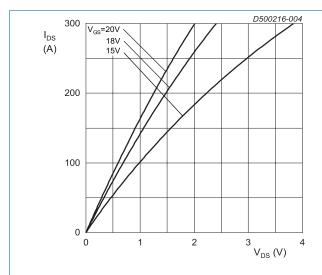


## 10. Characteristics

### **Table 7. Characteristics**

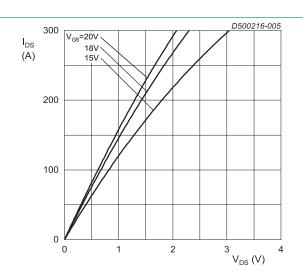
MOSFET							
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 400 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
$V_{GS(th)}$	gate-source threshold	$I_D = 48 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		1.9	2.5	3.5	V
	voltage	$I_D = 48 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 175 \text{ °C}$		-	1.9	-	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 1200 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	1	400	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 24 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	40	400	nA
	(absolute value)	V <sub>GS</sub> = -12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	40	400	nA
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 25 °C		-	8.0	-	mΩ
	resistance	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 25 °C		-	6.7	13	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 125 °C		-	10	-	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 150 °C		-	11	-	mΩ
	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 175 °C		-	11.6	-	mΩ	
$R_{G}$	gate resistance	$f = 1 \text{ MHz}$ ; $T_j = 25 \text{ °C}$ ; each die with $4.7\Omega$ $R_{G\text{-ext}}$ in series		-	1.27	-	Ω
g <sub>fs</sub>	transconductance	V <sub>DS</sub> = 20 V; I <sub>D</sub> = 150 A; T <sub>j</sub> = 25 °C		-	55	-	S
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 150 A; V <sub>DS</sub> = 800 V; V <sub>GS</sub> = 0 V/18 V;		-	536	-	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C		-	172	-	nC
$Q_{GD}$	gate-drain charge			-	102	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 1000 V; V <sub>GS</sub> = 0 V; f = 1 MHz;		-	13	-	nF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C		-	575	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	60	-	pF
E <sub>oss</sub>	Coss stored energy			-	290	-	μJ
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 800 V; V <sub>GS</sub> = -4 V/18 V;		-	82	-	ns
t <sub>r</sub>	rise time	$R_{G(off)}$ = 2.4 Ω; $R_{G(on)}$ = 2.4 Ω; $I_D$ = 150 A; L = 100 μH; $I_J$ = 25 °C		-	70	-	ns
$t_{d(off)}$	turn-off delay time			-	210	-	ns
t <sub>f</sub>	fall time			-	75	-	ns
E <sub>on</sub>	turn-on energy			-	5.4	-	mJ
E <sub>off</sub>	turn-off energy	1		_	2.3	_	mJ

Body dic	ode						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics		,				
$V_{\text{SD}}$	source-drain voltage	$V_{GS} = -4 \text{ V}; I_{SD} = 150 \text{ A}; T_j = 25 \text{ °C}$		-	5.8	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 150 \text{ A}; T_j = 150 \text{ °C}$		-	5.2	-	V
Dynamic	characteristics						
I <sub>rrm</sub>	reverse recovery current	$I_{SD} = 150 \text{ A}; V_{GS} = -4 \text{ V}/18 \text{ V}; V_{R} = 600 \text{ V};$		-	62	-	Α
t <sub>rr</sub>	reverse recovery time	di/dt = 2700 A/μs; $R_{G(ext)}$ = 5.1 Ω; $T_i$ = 25 °C		-	27	-	ns
Q <sub>r</sub>	recovered charge	J		-	928	-	nC
E <sub>rec</sub>	reverse recovery energy			-	61	-	μJ
NTC ther	mistor						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>25</sub>	Rated resistance	T <sub>NTC</sub> = 25 °C		-	5000	-	Ω
R <sub>100</sub>		T <sub>NTC</sub> = 100 °C			493±5%	)	Ω
B <sub>25/50</sub>	B-value	$R_2 = R_{25} \exp[B_{25/50}(1/T_2 - 1/(298.15K))]$		3380		K	
	Maximum operating temperature			-	200	-	°C
	Dissipation costant			-	2	-	mW/K
	Thermal time constant			-	≤10	-	s



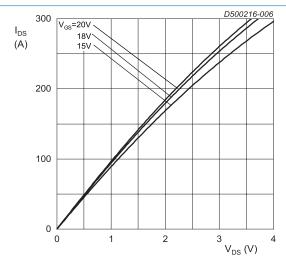
 $T_j = -40 \, ^{\circ}\text{C}; t_p < 200 \, \mu\text{s}$ 

Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



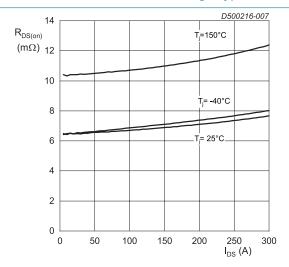
 $T_j$  = 25 °C;  $t_p$  < 200 µs

Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values

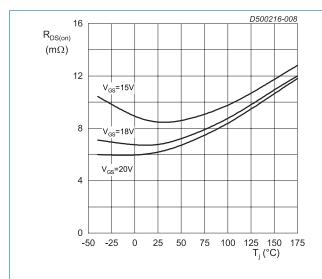


 $T_i = 150 \, ^{\circ}\text{C}; t_p < 200 \, \mu\text{s}$ 

Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

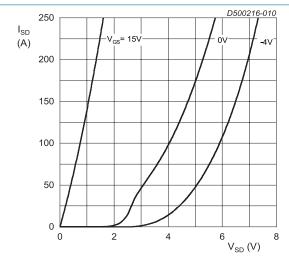


 $V_{GS} = 18 \ V; \ t_p < 200 \ \mu s$  Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



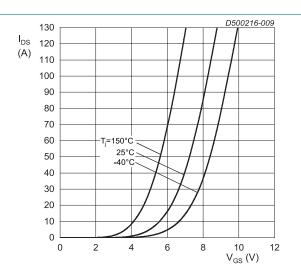
 $I_{DS}$  = 150 A;  $t_p$  < 200  $\mu$ s

Fig. 8. Drain-source on-state resistance as a function of junction temperature



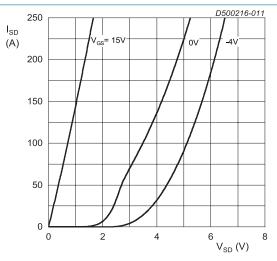
 $T_{j} = -40 \, ^{\circ}\text{C}; \, t_{p} < 200 \, \mu\text{s}$ 

Fig. 10. Body diode forward characteristics; typical values



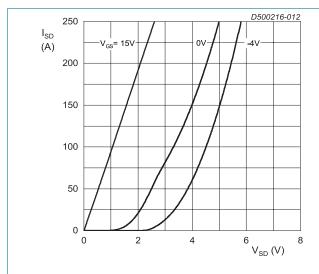
 $V_{DS} = 20 \text{ V}; t_p < 200 \text{ }\mu\text{s}$ 

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

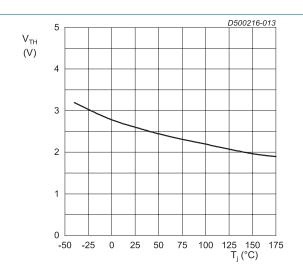


 $T_j = 25 \, ^{\circ}C; t_p < 200 \, \mu s$ 

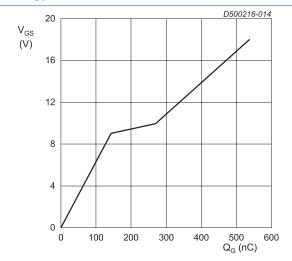
Fig. 11. Body diode forward characteristics; typical values



T<sub>j</sub> = 150 °C; t<sub>p</sub> < 200 μs Fig. 12. Body diode forward characteristics; typical values



V<sub>DS</sub> = 10 V; I<sub>DS</sub> = 48 mA Fig. 13. Threshold voltage as a function of junction temperature



 $I_{DS}$  = 150 A;  $I_{GS}$  = 0.1 mA;  $V_{DS}$  = 800 V;  $T_j$  = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

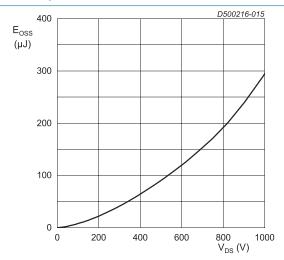
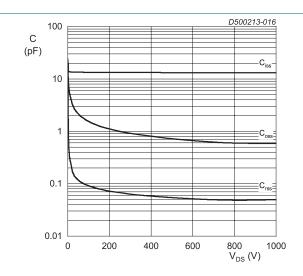
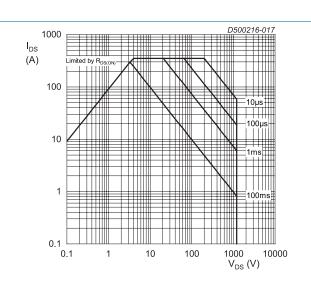


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



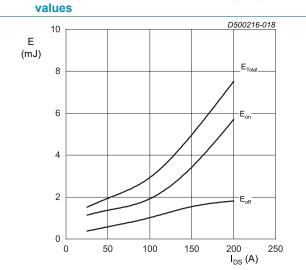
 $V_{DS} = 0 - 1000 \text{ V}$  $T_i = 25 \,^{\circ}\text{C}; V_{AC} = 25 \,\text{mV}; f = 1 \,\text{MHz}$ 

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical



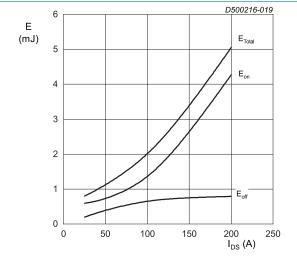
 $T_j = 25 \,^{\circ}\text{C}; D = 0$ Parameter:  $t_p$ 

Fig. 17. Forward bias safe operating area



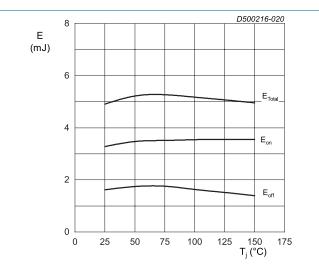
 $T_{j} = 25$  °C;  $V_{DD} = 800$  V;  $R_{G(off)} = 2.4$   $\Omega$ ;  $R_{G(on)} = 2.4$   $\Omega$ ;  $V_{GS} = -4$  V/18 V; L = 100 µH

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



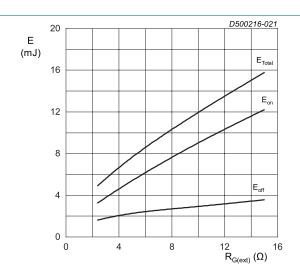
 $T_{j}$  = 25 °C;  $V_{DD}$  = 600 V;  $R_{G(off)}$  = 2.4  $\Omega;$   $R_{G(on)}$  = 2.4  $\Omega;$   $V_{GS}$  = -4 V/18 V; L = 100  $\mu H$ 

Fig. 19. Clamped Inductive Switching Energy as a function of drain current



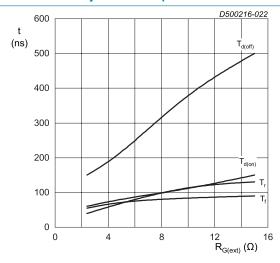
 $I_{DS}$  = 150 A;  $V_{DD}$  = 800 V;  $R_{G(off)}$  = 2.4  $\Omega;$   $R_{G(on)}$  = 2.4  $\Omega;$   $V_{GS}$  = -4 V/18 V; L = 100  $\mu H$ 

Fig. 20. Clamped Inductive Switching Energy as a function of junction temperature



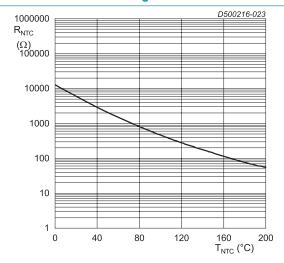
 $T_{\rm j}$  = 25 °C;  $V_{\rm DD}$  = 800 V;  $I_{\rm DS}$  = 150 A;  $V_{\rm GS}$  = -4 V/18 V;  $L = 100 \mu H$ 

Fig. 21. Clamped Inductive Switching Energy as a function of external gate resistance



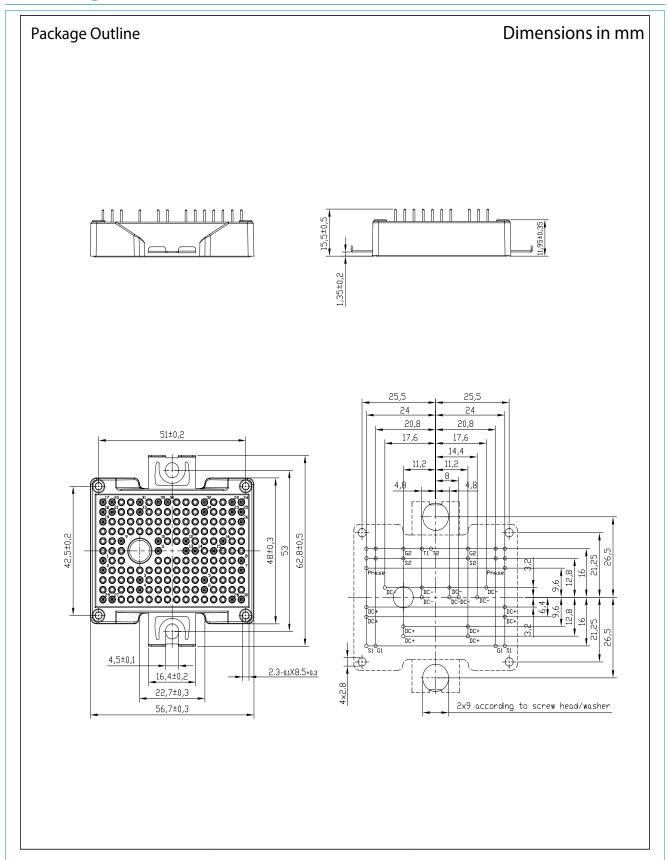
 $L = 100 \mu H$ 

Fig. 22. Switching time as a function of external gate resistance



 $T_i = 25$  °C;  $V_{DD} = 800$  V;  $I_{DS} = 150$  A;  $V_{GS} = -4$  V/18 V; Fig. 23. NTC thermistor resistance as a function of **NTC** temperature

## 11. Package outline



## 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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