

N-Channel Silicon Carbide MOSFET Module

Rev.01 - 23 October 2024

Product data sheet

1. General description

WeEnPACK-B2 module with WeEn 1200V Gen2 SiC MOSFET and Solderpin type. Integrated with NTC temperature sensor.



2. Features and benefits

- H Bridge topology
 - Solder pin type
 - Low R_{DSon}
 - Low Switching Losses
 - Low Q_g and C_{rss}
 - Low Inductive Design

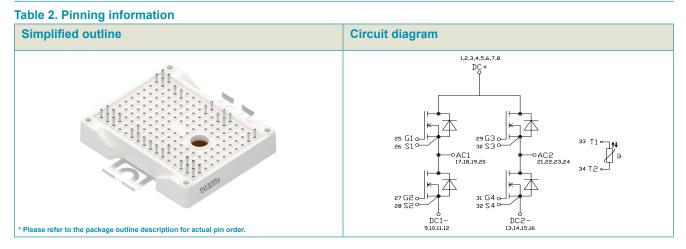
3. Applications

- Power inverters
- AC-DC converters
- Active power factor correctors
- Motor drives

4. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage	T _j = 25 °C			1200		V
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C			104		А
P _{tot}	total power dissipation	T _h = 25 °C			142		W
Tj	junction temperature			-40 to 150			°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics	·					
$R_{\text{DS(on)}}$	drain-source on-state resistance	V _{GS} = 15 V; I _D = 100 A; T _j = 25 °C		-	11	-	mΩ
		V _{GS} = 18 V; I _D = 100 A; T _j = 25 °C		-	8.3	-	mΩ
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_{D} = 100 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	402	-	nC
Q_{GD}	gate-drain charge	T _j = 25 °C		-	62	-	nC
Source-d	rain diode						
Qr	recovered charge	I _{SD} = 100 A; V _{GS} = -4 V; di/dt = 6500 A/μs; V _R = 600 V; T _i = 25 °C		-	950	-	nC

5. Pinning information



6. Ordering information

Table 3. Ordering information								
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date		
WMSC011F12B2S	WeEnPACK-B2	WMSC011F12B2S6T	Tray	12	WeEnPACK- B2PFB-A	28-Jun-2024		

7. Marking

Table 4. Marking codes							
	Type number	Marking codes					
	WMSC011F12B2S	WMSC011F12B2S					

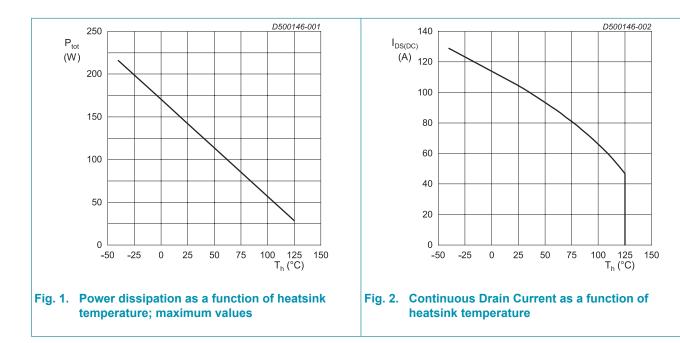
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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
T _{stg}	storage temperature			-40 to 125	°C
T _{j.op}	operating junction temperature			-40 to 150	°C
T _{j.max}	maximum junction temperature	Intermittent condition with shortened lifetime		-40 to 175	°C
V _{ISOL}	RMS isolation voltage	T _j = 25 °C; all terminals shorted; f = 50 Hz; t = 1 s		3500	V
MOSFET			·		
V _{DS}	drain-source voltage	T _j = 25 °C		1200	V
V _{GS,max}	gate-source voltage	Absolute maximum values		-12 to 24	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P _{tot}	total power dissipation	T _h = 25 °C		142	W
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C		104	А
		V _{GS} = 18 V; T _h = 100 °C		66	Α
I _{DM}	peak drain current	pulse width t_p limited by T_{jmax}	Fig.17	208	Α
E _{as}	single pulse drain-to- source avalanche	$I_{AS} = 30 \text{ A}; \text{ L} = 1 \text{ mH}; \text{ V}_{DD} = 100 \text{ V};$ $T_{j(init)} = 25 \text{ °C}; \text{ per MOSFET}$		450	mJ
Body Dioc	le				
I _{SD}	DC body diode forward current	V _{GS} = -4 V; T _h = 25 °C		36	А
I _{SD,pulse}	Pulse body diode current	verified by design, t_p limited by T_{imax}		208	Α

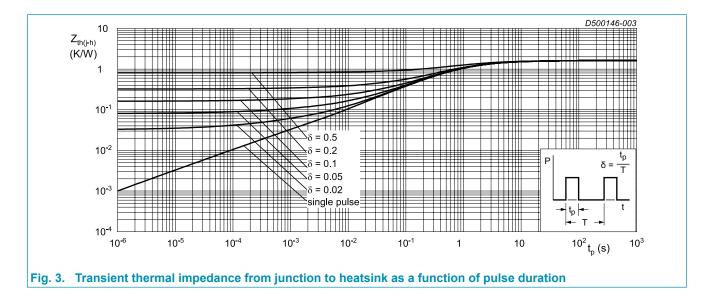


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9. Thermal characteristics

Table 6. Th	ermal characteristics						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	per MOSFET		-	0.26	-	K/W
$R_{\text{th(j-h)}}$	thermal resistance from junction to heatsink	per MOSFET, $\lambda_{grease} = 1 \text{ W/(m·K)}$ thick _{grease} = 50 um		-	0.88	-	K/W
Internal Is	solation	basic insulation (class 1, IEC 61140)		Al ₂ O ₃			
d _{Creep}	Creepage distance	terminal to heatsink		-	11.5	-	mm
		terminal to terminal		-	6.3	-	mm
d _{Clear}	Clearance	terminal to heatsink		-	10	-	mm
		terminal to terminal		-	5	-	mm
CTI	Comperative tracking index				>200		
F	Mounting force per clamp			40	-	80	Ν
G	Approximate Weight			-	36	-	g

Note: Module is ESD sensitive. Handling precautions are recommended.

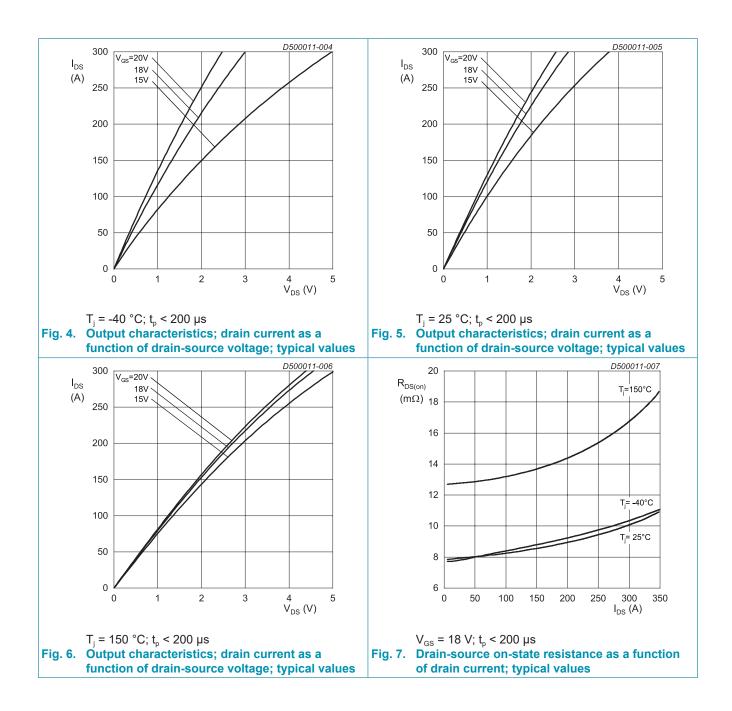


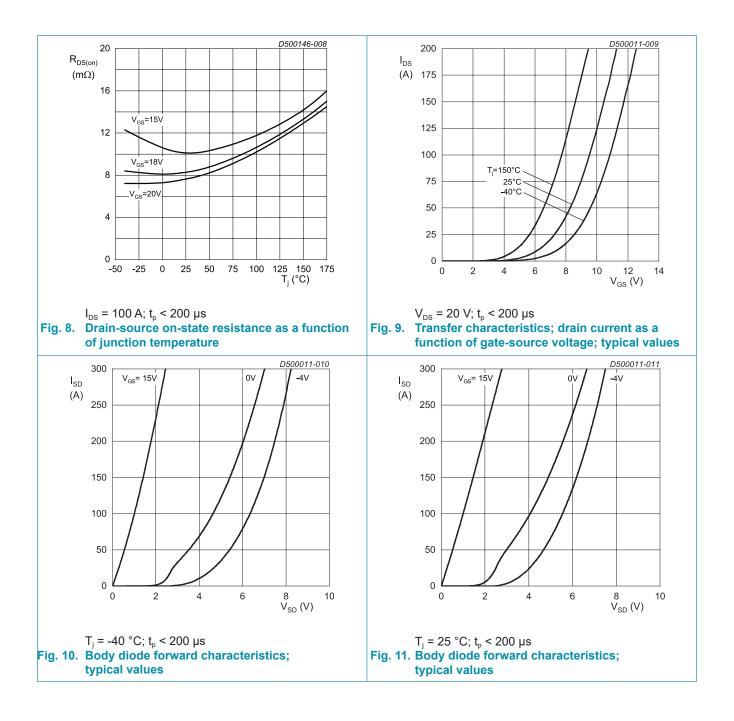
10. Characteristics

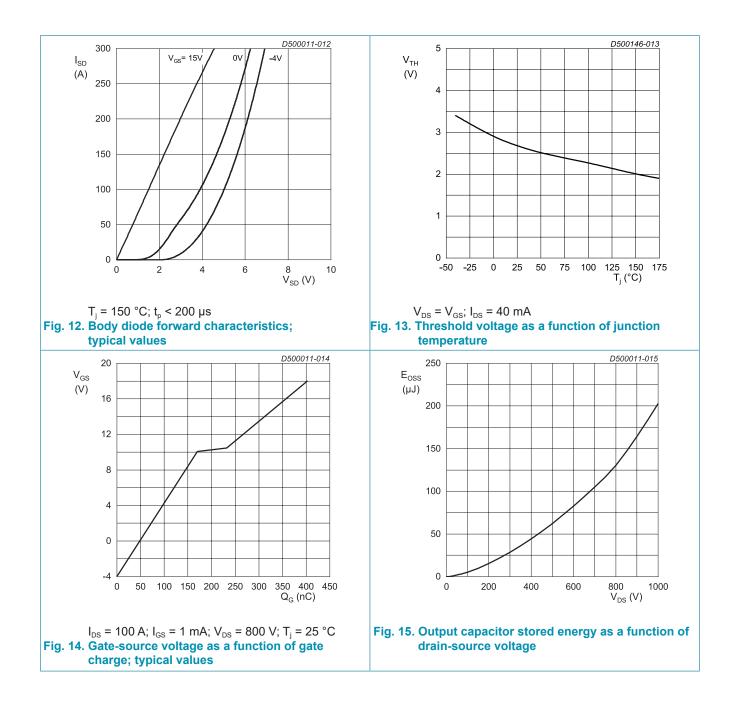
Table 7. Characteristics

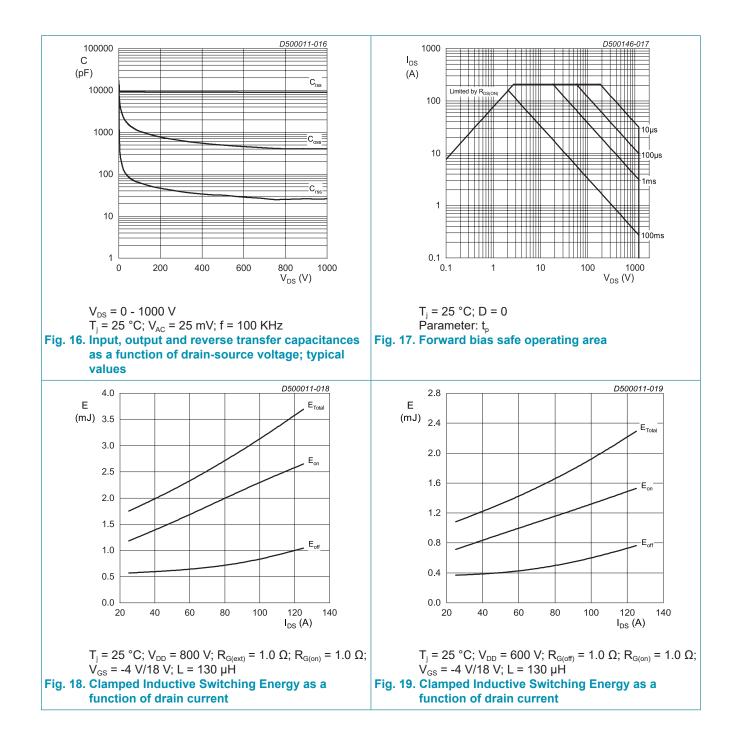
MOSFET							
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	I_{D} = 200 µA; V_{GS} = 0 V; T_{j} = 25 °C		1200	-	-	V
V _{GS(th)}	gate-source threshold	$I_{D} = 40 \text{ mA}; V_{DS} = V_{GS}; T_{j} = 25 \text{ °C}$		1.9	2.5	3.5	V
	voltage	I _D = 40 mA; V _{DS} = V _{GS} ; T _j = 175 °C		-	1.9	-	V
I _{DSS}	drain leakage current	V_{DS} = 1200 V; V_{GS} = 0 V; T_j = 25 °C		-	0.4	200	μA
I _{GSS}	gate leakage current	V_{GS} = 24 V; V_{DS} = 0 V; T_j = 25 °C		-	20	200	nA
	(absolute value)	V _{GS} = -12 V; V _{DS} = 0 V; T _j = 25 °C		-	20	200	nA
$R_{\text{DS(on)}}$	drain-source on-state	V _{GS} = 15 V; I _D = 100 A; T _j = 25 °C		-	11	-	mΩ
	resistance	V_{GS} = 18 V; I _D = 100 A; T _j = 25 °C		-	8.3	14	mΩ
		V _{GS} = 18 V; I _D = 100 A; T _j = 125 °C		-	11.7	-	mΩ
		V _{GS} = 18 V; I _D = 100 A; T _j = 150 °C		-	13.2	-	mΩ
		V _{GS} = 18 V; I _D = 100 A; T _j = 175 °C		-	13.9	-	mΩ
R_{G}	gate resistance, each side	f = 1 MHz; T _j = 25 °C, each die with 4.7 Ω R _{G,ext} in series		-	2.85	-	Ω
$g_{\rm fs}$	transconductance	V _{DS} = 20 V; I _D = 100 A; T _j = 25 °C		-	56	-	S
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_{D} = 100 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	402	-	nC
Q_{GS}	gate-source charge	T _j = 25 °C		-	170	-	nC
Q_{GD}	gate-drain charge			-	62	-	nC
C_{iss}	input capacitance	V _{DS} = 1000 V; V _{GS} = 0 V; f = 100 KHz;		-	9	-	nF
C _{oss}	output capacitance	$T_j = 25 \ ^{\circ}C$		-	405	-	pF
C _{rss}	reverse transfer capacitance			-	26	-	pF
E _{oss}	Coss stored energy			-	203	-	μJ
t _{d(on)}	turn-on delay time	$V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	29	-	ns
t _r	rise time	R _{G(ext)} = 1.0 Ω; I _D = 100 A; L = 130 μH; T _i = 25 °C		-	13	-	ns
$t_{\rm d(off)}$	turn-off delay time			-	84	-	ns
t _f	fall time			-	40	-	ns
E _{on}	turn-on energy			-	2.3	-	mJ
E _{off}	turn-off energy			-	0.82	-	mJ

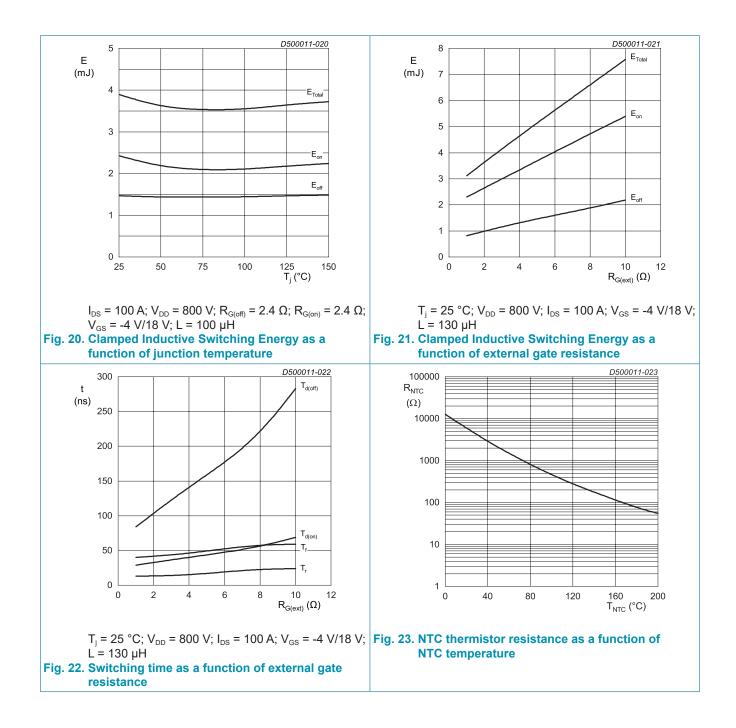
Body dio	de						
Symbol	Parameter	Conditions Notes Min Typ Ma		Max	Unit		
Static ch	aracteristics	·					
V_{SD}	source-drain voltage	V_{GS} = -4 V; I _{SD} = 100 A; T _j = 25 °C		-	5.5	-	V
		V _{GS} = -4 V; I _{SD} = 100 A; T _j = 150 °C		-	5.0	-	V
Dynamic	characteristics				-		
t _{rr}	reverse recovery time	I_{SD} = 100 A; V_{GS} = -4 V; di/dt = 6500 A/µs;		-	23	-	ns
Q _r	recovered charge	V _R = 600 V; T _j = 25 °C		-	950	-	nC
I _{rrm}	reverse recovery current			-	72	-	А
E _{rec}	reverse recovery energy			-	370	-	μJ
t _{rr}	reverse recovery time	I_{SD} = 100 A; V_{GS} = -4 V; di/dt = 8000 A/µs;		-	30	-	ns
Q _r	recovered charge	V _R = 600 V; T _j = 150 °C		-	2436	-	nC
l _{rrm}	reverse recovery current			-	125	-	А
E _{rec}	reverse recovery energy			-	1210	-	μJ
NTC ther	mistor						,
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R ₂₅	Rated resistance	T _{NTC} = 25 °C		-	5000	-	Ω
R ₁₀₀		T _{NTC} = 100 °C		465±5%		Ω	
B _{25/50}	B-value	$R_2 = R_{25} \exp[B_{25/50}(1/T_2 - 1/(298.15K))]$			3380		К
	Maximum operating temperature			-	200	-	°C
	Dissipation costant			-	2	-	mW/K
	Thermal time constant			-	≤10	-	s



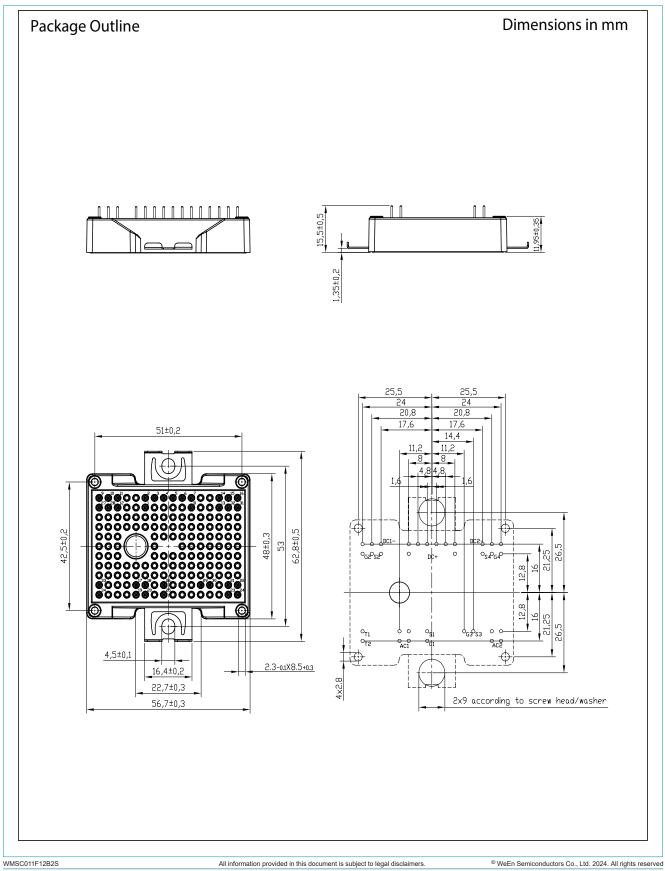








11. Package outline



N-Channel Silicon Carbide MOSFET Module

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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