**Product data sheet** 

## 1. General description

Planar passivated Silicon Controlled Rectifier (SCR) module in TO-240AA for use in applications requiring high blocking voltage capability, high inrush current capability and high thermal cycling performance.

### 2. Features and benefits

- · High blocking voltage capability
- · High thermal cycling performance
- · Planar passivated for voltage ruggedness and reliability
- Package meets UL certification
- Package is RoHS compliant
- · Industry standard outline
- · Soldering pins for PCB mounting
- Copper base plate
- · Cathode Kelvin contacts provided
- UL1557 certified (Document number E346397)

## 3. Applications

- Softstart AC motor control
- DC Motor control
- AC power control
- Power converter
- Temperature control
- Lighting control

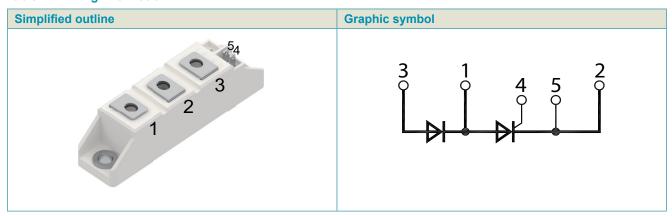
### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit	
Absolute maximum rating								
$V_{DRM}$	repetitive peak forward voltage				1600		V	
$V_{RRM}$	repetitive peak reverse voltage				1600		V	
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave			182		Α	
$I_{T(AV)}/I_{F(AV)}$	average forward current	δ = 0.5 ; square-wave pulse			116		Α	
$I_{TSM}/I_{FSM}$	non-repetitive peak on-	half sine wave; $T_{j(init)} = 25 \text{ °C}$ ; $t_p = 10 \text{ ms}$		2300 2000			А	
	state current	half sine wave; $T_{j(init)}$ = 150 °C; $t_p$ = 10 ms					Α	
		half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 8.3 ms			2530		Α	
		half sine wave; $T_{j(init)}$ = 150 °C; $t_p$ = 8.3 ms			2200		А	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Static ch	aracteristics							
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$		30	-	100	mA	
$V_{GT}$	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	0.70	1.20	V	
V <sub>T</sub>	on-state voltage	I <sub>τ</sub> = 116 A; T <sub>j</sub> = 25 °C		-	-	1.29	V	
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 116 A; T <sub>J</sub> = 25 °C		-	-	1.29	V	

## 5. Pinning information

**Table 2. Pinning information** 



# 6. Ordering information

**Table 3. Ordering information** 

Type number	Package Name	Orderable part number	Packing method		Package version	Package issue date
	Ivaille		memou	quantity	version	155ue uale
WHMH116T16	TO-240AA	WHMH116T16T	Tray	12	WeEnPACK-	18-Apr-2024
					20mmPHB-D	

## 7. Marking

### **Table 4. Marking codes**

Type number	Marking codes
WHMH116T16	WHMH116T16

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
$V_{\text{DRM}}$	repetitive peak forward voltage			1600	V
$V_{RRM}$	repetitive peak reverse voltage			1600	V
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave		182	А
$I_{T(AV)}/I_{F(AV)}$	average forward current	$\delta$ = 0.5 ; square-wave pulse		116	А
I <sub>TSM</sub> /I <sub>FSM</sub>	non-repetitive peak onstate	half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 10 ms		2300	А
	current	half sine wave; $T_{j(init)}$ = 150 °C; $t_p$ = 10 ms		2000	А
		half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 8.3 ms		2530	А
		half sine wave; $T_{j(init)}$ = 150 °C; $t_p$ = 8.3 ms		2200	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse		26.5	kA²s
dl <sub>⊤</sub> /dt	rate of rise of on-state current	$I_G = 200 \text{ mA}; T_j = 150 \text{ °C}$		200	A/µs
I <sub>GM</sub>	peak gate current			10	А
$V_{RGM}$	peak reverse gate voltage			5	V
$P_{GM}$	peak gate power			20	W
$P_{G(AV)}$	average gate power	over any 20 ms period		0.5	W
$T_{vj}$	virtual junction temperature			-40 to 150	°C
T <sub>op</sub>	operation temperature			-40 to 130	°C
T <sub>stg</sub>	storage temperature			-40 to 130	°C

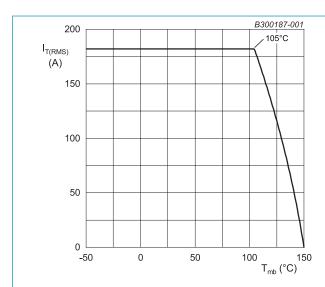


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values

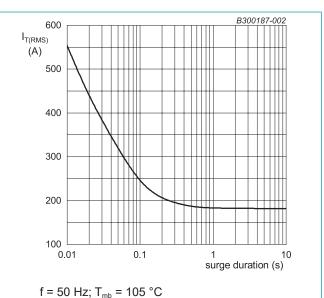


Fig. 2. RMS on-state current as a function of surge duration; maximum values

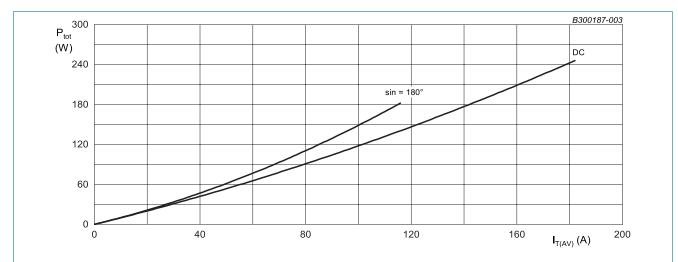
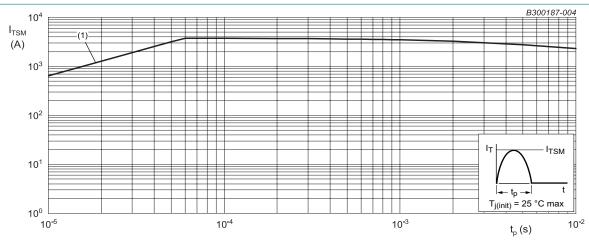


Fig. 3. Total power dissipation as a function of average on-state current; maximum values; per thyristor



 $t_p \le 10 \text{ ms}$ (1)  $dI_T/dt \text{ limit}$ 

Fig. 4. Non-repetitive peak on-state current as a function of pulse width; maximum values

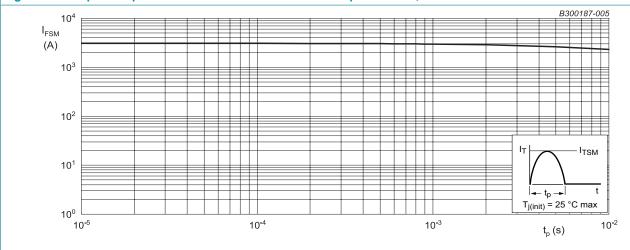


Fig. 5. Non-repetitive peak on-state current as a function of pulse width; maximum values

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-c)</sub>	$R_{\text{th(j-c)}}$ thermal resistance from junction to case	per thyristor/diode		-	-	0.25	K/W
		per module		-	-	0.125	K/W
R <sub>th(j-h)</sub>		per thyristor/diode		-	-	0.48	K/W
	junction to heatsink	per module		-	-	0.24	K/W

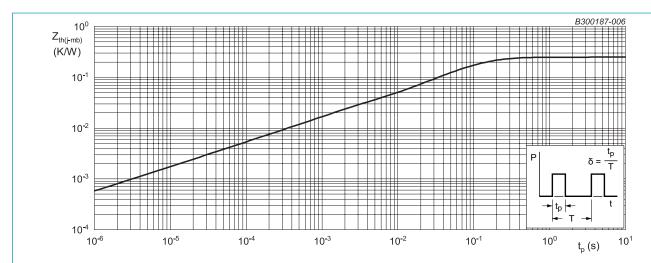


Fig. 6. Transient thermal impedance from junction to mounting base as a function of pulse duration; per thyristor

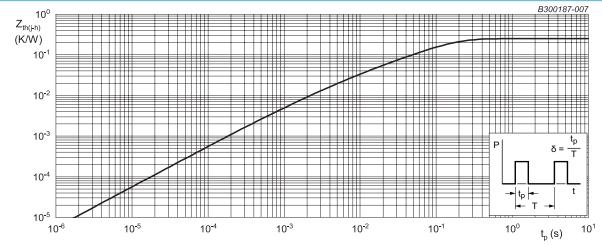


Fig. 7. Transient thermal impedance from junction to mounting base as a function of pulse duration; per diode

## 10. Package characteristics

**Table 7. Isolation characteristics** 

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
V <sub>isol</sub>	isolation voltage	50/60 Hz; RMS; $I_{ISOL} \le 1$ mA; t = 1 second; AC		-	-	3600	V
		50/60 Hz; RMS; I <sub>ISOL</sub> ≤ 1 mA; t = 1 minute; AC		-	-	3000	V

WHMH116T16

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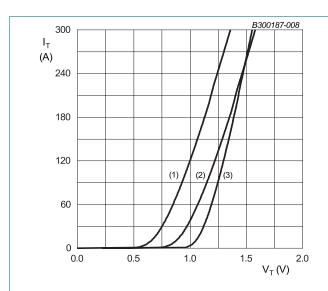
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# 11. Characteristics

### Table 8. Characteristics

Thyristor							
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics			,			
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$		30	-	100	mA
$V_{\text{GT}}$	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 25 °C		-	0.70	1.20	V
		$V_D = 2/3 V_{DRM}$ ; $I_T = 0.1 A$ ; $T_j = 150 °C$		0.2	0.4	-	V
$I_{GD}$	gate non-trigger current	T <sub>j</sub> = 150 °C		-	-	7.0	mA
$V_{GD}$	gate non-trigger voltage	T <sub>j</sub> = 150 °C		-	-	0.17	V
I <sub>L</sub>	latching current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	-	300	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C		-	-	200	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 116 A; T <sub>j</sub> = 25 °C		-	-	1.29	V
V <sub>TO</sub>	threshold voltage	T <sub>j</sub> = 150 °C		-	-	0.969	V
r <sub>T</sub>	slope resistance	T <sub>j</sub> = 150 °C		-	-	2.1	mΩ
I <sub>D</sub>	off-state current	V <sub>D</sub> = 1600 V; T <sub>j</sub> = 25 °C		-	-	100	μΑ
		V <sub>D</sub> = 1600 V; T <sub>j</sub> = 150 °C		-	-	15	mA
I <sub>R</sub>	reverse current	V <sub>R</sub> = 1600 V; T <sub>j</sub> = 25 °C		-	-	100	μΑ
		V <sub>R</sub> = 1600 V; T <sub>j</sub> = 150 °C		-	-	15	mA
Dynamic	characteristics						
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 1072 V; $T_j$ = 150 °C; $(V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit		1500	-	-	V/µs
$\mathbf{t}_{gt}$	gate-controlled turn-on time	$I_{TM} = 40 \text{ A}; V_D = 800 \text{ V}; I_G = 100 \text{ mA}; $ $(dI_G/dt)_M = 1 \text{ A/}\mu\text{s}; T_j = 25 \text{ °C}$		-	2	-	μs
t <sub>q</sub>	commutated turn-off time	$I_{TM} = 2 \text{ A}; t_p = 50  \mu\text{s};  dV/dt = 5  V/\mu\text{s}; $ $dI/dt = 30  A/\mu\text{s}; T_i = 25 ^{\circ}\text{C}$		-	150	-	μs

Diode							
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics		`				
V <sub>F</sub> forward voltage		I <sub>F</sub> = 116 A; T <sub>j</sub> = 25 °C		-	1.15	1.29	V
		I <sub>F</sub> = 116 A; T <sub>j</sub> = 150 °C		-	-	1.20	V
Vo	threshold voltage	T <sub>j</sub> = 150 °C		-	-	0.933	V
Rs	slope resistance	T <sub>j</sub> = 150 °C		-	-	2.4	mΩ
I <sub>R</sub>	reverse current	V <sub>R</sub> = 1600 V; T <sub>j</sub> = 25 °C		-	-	100	μA
		V <sub>R</sub> = 1600 V; T <sub>j</sub> = 150 °C		-	-	15	mA



 $V_{TO}$  = 0.969 V;  $r_{T}$  = 0.0021  $\Omega$ 

(1) T<sub>j</sub> = 150 °C; typical values (2) T<sub>j</sub> = 150 °C; maximum values

(3) T<sub>i</sub> = 25 °C; maximum values

Fig. 8. Thyristor on-state current as a function of on-state voltage

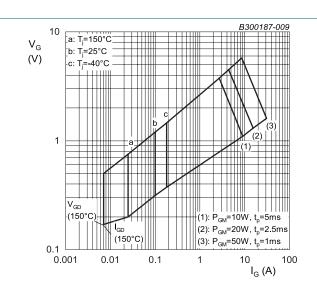
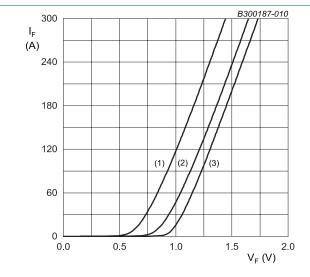


Fig. 9. Gate voltage as a function of gate current



 $V_{\rm O}$  = 0.933 V;  $R_{\rm S}$  = 0.0024  $\Omega$ 

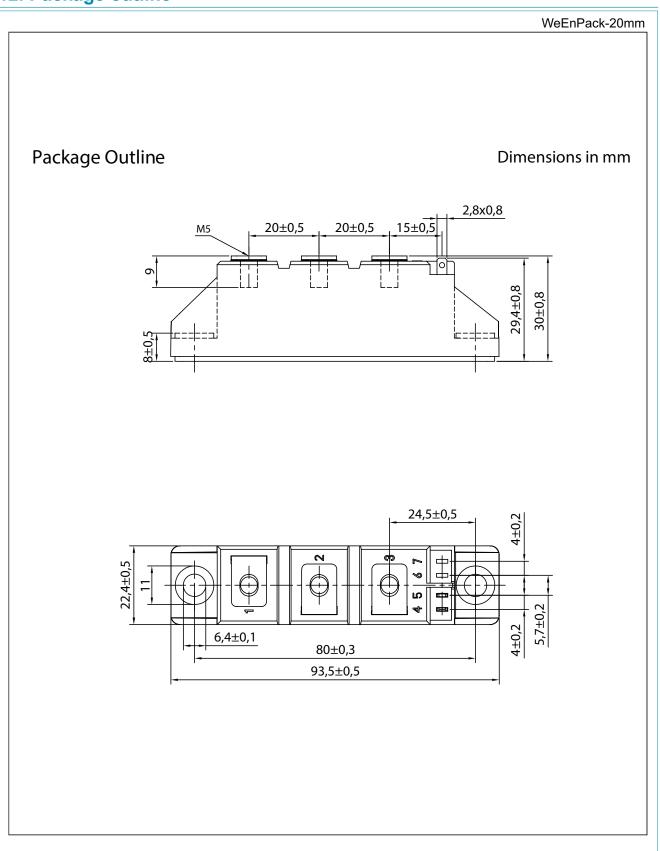
(1) T<sub>i</sub> = 150 °C; typical values

(2) T<sub>j</sub> = 150 °C; maximum values

(3) T<sub>i</sub> = 25 °C; maximum values

Fig. 10. Diode forward current as a function of forward voltage

# 12. Package outline



### 13. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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- Please consult the most recently issued document before initiating or completing a design.
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**WHMH116T16** 

**SCR Module** 

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