**Product data sheet** 

## 1. General description

Silicon Carbide MOSFET in a TO263-7L plastic package, designed for high frequency, high efficiency systems.



### 2. Features and benefits

- Kelvin source configuration
- · Low specific on-resistance
- Optimized dynamic performance
- 0V turn-off V<sub>GS</sub> for simple gate driving
- 100% UIS Tested
- Easy to parallel
- RoHS compliant
- Automotive Qualified (AEC-Q101)

## 3. Applications

- · Automotive on board chargers
- Automotive DC-DC converters
- · Automotive electric compressor motor drives
- · HV battery management systems

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values		•	Unit
Absolute	maximum rating						
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C			1200		V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C			24		Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C			136		W
T <sub>j</sub>	junction temperature			-55 to 175		°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 10 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	150	-	mΩ
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	$I_D = 10 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	40	-	nC
$Q_{GD}$	gate-drain charge	T <sub>j</sub> = 25 °C		-	9.8	-	nC
Source-di	rain diode						
$Q_r$	recovered charge	$I_{SD}$ = 10 A; di/dt = 500 A/ $\mu$ s; $V_{DS}$ = 400 V; $T_{j}$ = 25 °C		-	26	-	nC

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	<b>D</b>
2	SS	source sense		
3-7	S	source		G
mb	D	mounting base; connected to drain	TO263-7L	SS Sym301 S

# 6. Ordering information

**Table 3. Ordering information** 

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M150120B7-A	TO263-7L	WNSC2M150120B7-A6J	Reel	800	TO263P-7L	05-Mar-2024

## 7. Marking

#### Table 4. Marking codes

Type number	Marking codes
WNSC2M150120B7-A	WNSC2M 150120B7-A

# 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		1200	V
$V_{\rm GS,max}$	gate-source voltage			-10 to 22	V
$V_{GS,op}$	gate-source voltage			-4 to 18	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C		136	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C		24	А
		V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 100 °C		17	Α
I <sub>DM</sub>	peak drain current	pulse width t <sub>p</sub> limited by T <sub>jmax</sub>	Fig.17	48	Α
Is	continuous diode current	V <sub>GS</sub> = -4 V; T <sub>mb</sub> = 25 °C		25	Α
I <sub>SM</sub>	pulse diode current	$V_{GS}$ = -4 V; pulse width $t_p$ limited by $T_{jmax}$		48	А
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS} = 7 \text{ A}; L = 1 \text{ mH}; V_{DD} = 100 \text{ V};$ $T_j = 25 \text{ °C}$		25	mJ
T <sub>stg</sub>	storage temperature			-55 to 175	°C
T <sub>j</sub>	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

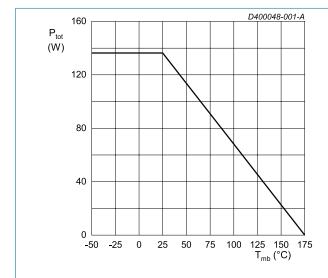


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

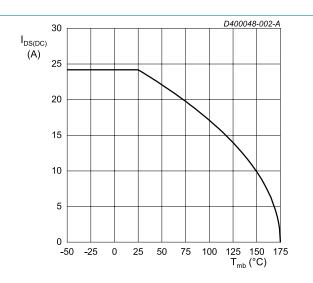


Fig. 2. Continuous Drain Current as a function of mounting base temperature

### 9. Thermal & Mechanical characteristics

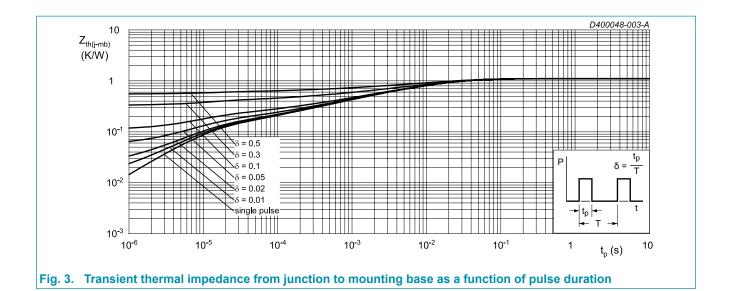
**Table 6. Thermal & Mechanical characteristics** 

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	1.1	-	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.

Do not use self-tapping screws.

Device is ESD sensitive. Handling precautions are recommended.



### 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	$I_D = 2.5 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		1.9	2.6	3.5	V
	voltage	$I_D = 2.5 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 175 \text{ °C}$		-	1.9	-	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.2	100	μA
		V <sub>DS</sub> = 1200 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C		-	2	-	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS} = 24 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$		-	10	100	nA
		$V_{GS} = -12 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	10	100	nA
R <sub>DS(on)</sub>	drain-source on-state	$V_{GS} = 15 \text{ V}; I_D = 10 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	150	-	mΩ
	resistance	$V_{GS} = 18 \text{ V}; I_D = 10 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	120	150	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 175 °C		-	233	-	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		-	3	-	Ω
g <sub>fs</sub>	transconductance	$V_{DS} = 20 \text{ V}; I_D = 10 \text{ A}; T_j = 25 \text{ °C}$		-	5.1	-	S
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	$I_D = 10 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	40	-	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C		-	15	-	nC
$Q_{GD}$	gate-drain charge			-	9.8	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 1000 V; V <sub>GS</sub> = 0 V; f = 1 MHz;		-	741	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C		-	36	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	3.4	-	pF
E <sub>oss</sub>	Coss stored energy			-	18	-	μJ
t <sub>d(on)</sub>	turn-on delay time	$V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V}; R_{G(ext)} = 5.1$		-	17	-	ns
t <sub>r</sub>	rise time	$\Omega$ ; $I_D = 10 \text{ A}$ ; $L = 330 \mu\text{H}$ ; $T_j = 25 \degree \text{C}$		-	12	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	23	-	ns
t <sub>f</sub>	fall time			-	15	-	ns
E <sub>on</sub>	turn-on energy (Body Diode FWD)		Fig.20	-	92	-	μJ
E <sub>off</sub>	turn-off energy (Body Diode FWD)		Fig.20	-	13	-	μJ
Source-di	rain diode						
V <sub>SD</sub>	source-drain voltage	$V_{GS} = 0 \text{ V; } I_{SD} = 5 \text{ A; } T_j = 25 \text{ °C}$		-	3.2	-	V
		V <sub>GS</sub> = -4 V; I <sub>SD</sub> = 5 A; T <sub>j</sub> = 25 °C		-	4.8	-	V
		V <sub>GS</sub> = -4 V; I <sub>SD</sub> = 5 A; T <sub>j</sub> = 175 °C		-	4.2	-	V
t <sub>rr</sub>	reverse recovery time	$I_{SD} = 10 \text{ A}$ ; di/dt = 500 A/ $\mu$ s; $V_{DS} = 400 \text{ V}$ ;		-	17	-	ns
Q <sub>r</sub>	recovered charge	T <sub>j</sub> = 25 °C		-	26	-	nC
I <sub>rrm</sub>	reverse recovery current			-	2.7	-	Α

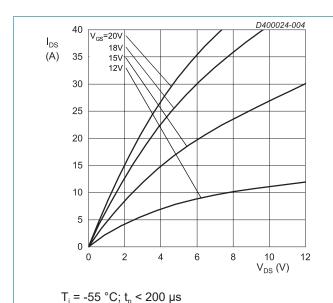
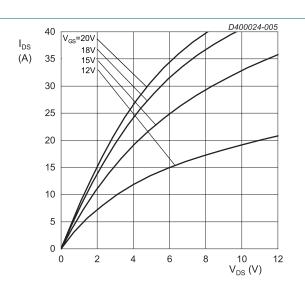
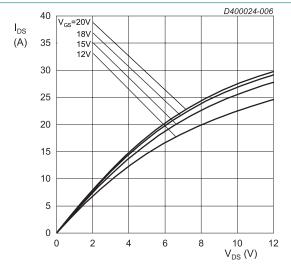


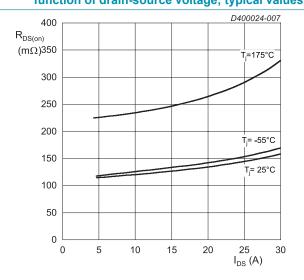
Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



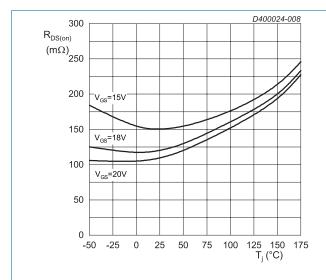
T<sub>j</sub> = 25 °C; t<sub>p</sub> < 200 μs Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



 $T_j$  = 175 °C;  $t_p$  < 200 µs Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

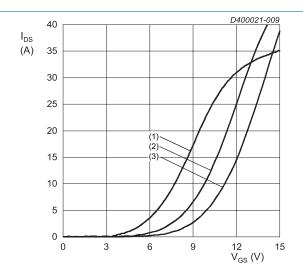


 $V_{GS}$  = 18 V;  $t_p$  < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



 $I_{DS}$  = 10 A;  $t_p$  < 200  $\mu s$ 

Fig. 8. Drain-source on-state resistance as a function of junction temperature



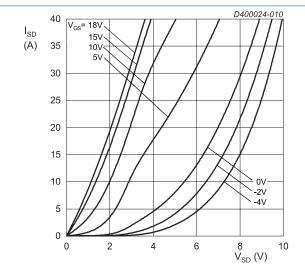
$$V_{DS}$$
 = 20 V;  $t_p$  < 200  $\mu s$ 

(1) 
$$T_j = 175 \,^{\circ}\text{C}$$
  
(2)  $T_j = 25 \,^{\circ}\text{C}$ 

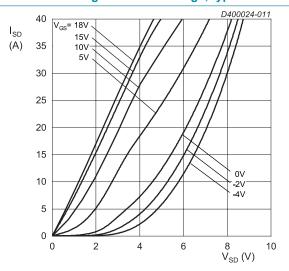
$$(2) T_i = 25 °C$$

(3)  $T_i = -55 \,^{\circ}C$ 

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

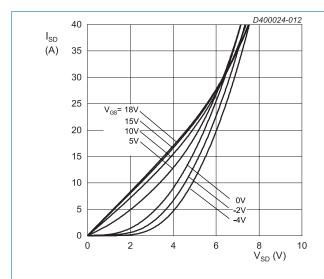


 $T_j = -55 \, ^{\circ}\text{C}; t_p < 200 \, \mu\text{s}$ Fig. 10. Body diode forward characteristics; typical values

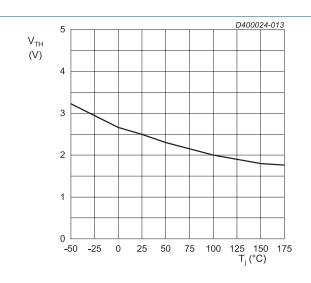


 $T_{j} = 25 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ 

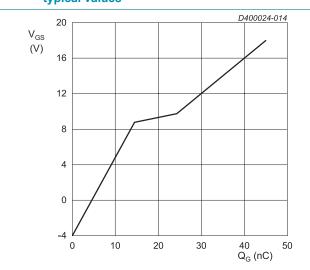
Fig. 11. Body diode forward characteristics; typical values



 $T_{j}$  = 175 °C;  $t_{p}$  < 200 µs Fig. 12. Body diode forward characteristics; typical values



V<sub>DS</sub> = 10 V; I<sub>DS</sub> = 2.5 mA Fig. 13. Threshold voltage as a function of junction temperature



I<sub>DS</sub> = 10 A; I<sub>GS</sub> = 0.1 mA; V<sub>DS</sub> = 800 V; T<sub>j</sub> = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

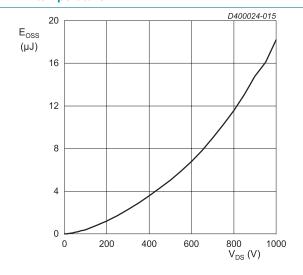
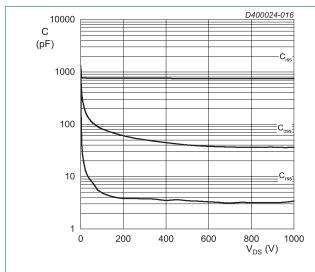


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



 $V_{DS} = 0 - 1000 V$ 

 $T_j = 25 \, ^{\circ}C; \, V_{AC} = 25 \, \text{mV}; \, f = 1 \, \text{MHz}$ 

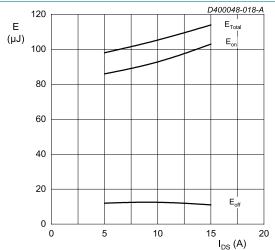
10 Limited by R<sub>DS(ON)</sub> 100µs 1000ms 10000 V<sub>DS</sub> (V)

 $T_j = 25 \text{ °C; D} = 0$ Parameter:  $t_p$ 

100

Fig. 17. Forward bias safe operating area

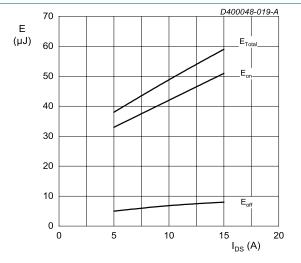




 $T_{j}$  = 25 °C;  $V_{DD}$  = 800 V;  $R_{G(ext)}$  = 5.1  $\Omega;$   $V_{GS}$  = -4 V/18 V; L = 330  $\mu H$ 

FWD = WNSC2M150120B7-A

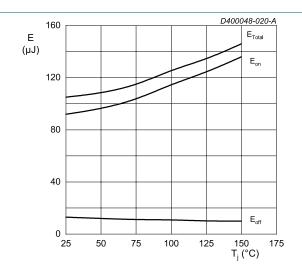
Fig. 18. Clamped Inductive Switching Energy as a function of drain current



 $T_{j}$  = 25 °C;  $V_{DD}$  = 600 V;  $R_{G(ext)}$  = 5.1  $\Omega;$   $V_{GS}$  = -4 V/18 V; L = 330  $\mu H$ 

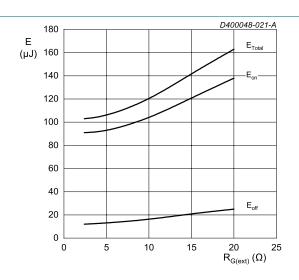
FWD = WNSC2M150120B7-A

Fig. 19. Clamped Inductive Switching Energy as a function of drain current



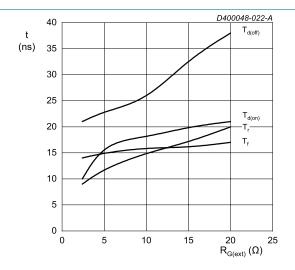
$$\begin{split} I_{DS} &= 10 \text{ A; V}_{DD} = 800 \text{ V; R}_{G(ext)} = 5.1 \text{ }\Omega; \\ V_{GS} &= -4 \text{ V}/18 \text{ V; L} = 330 \text{ }\mu\text{H} \\ FWD &= WNSC2M150120B7-A \end{split}$$

Fig. 20. Clamped Inductive Switching Energy as a function of junction temperature



 $T_{\rm j}$  = 25 °C;  $V_{\rm DD}$  = 800 V;  $I_{\rm DS}$  = 10 A;  $V_{\rm GS}$  = -4 V/18 V FWD = WNSC2M150120B7-A; L = 330  $\mu H$ 

Fig. 21. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_j$  = 25 °C;  $V_{DD}$  = 800 V;  $I_{DS}$  = 10 A;  $V_{GS}$  = -4 V/18 V FWD = WNSC2M150120B7-A; L = 330  $\mu$ H Fig. 22. Switching time as a function of external gate

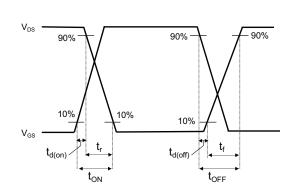
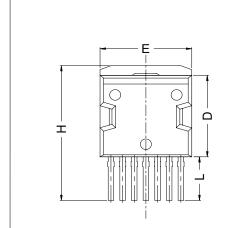
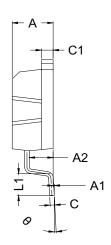


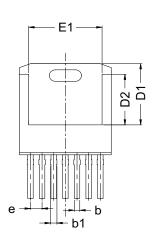
Fig. 23. Switching time definition

resistance

# 11. Package outline

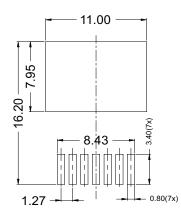






Dim	All Dimensions in Millimeters				
Dilli	Min	Тур	Max		
Α	4.30	4.46	4.60		
A1	0	0.13	0.25		
A2	2.50	2.60	2.70		
b	0.50	0.60	0.70		
b1	0.50	0.70	0.90		
С	0.40	0.52	0.60		
C1	1.17	1.29	1.40		
D	9.00	9.25	9.50		
D1	6.80	6.95	7.10		
D2	5.60	5.75	5.90		
E	9.80	10.00	10.20		
E1	7.90	8.00	8.10		
е		1.27 BSC			
Н	14.60	15.30	16.00		
L	4.50	4.95	5.40		
L1	2.10	2.47	2.80		
θ	0°	4°	8°		

## Footprint:



### 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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# WNSC2M150120B7-A

**N-Channel Silicon Carbide MOSFET** 

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

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### 13. Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Marking	2
8. Limiting values	3
9. Thermal & Mechanical characteristics	4
10. Characteristics	5
11. Package outline	11
12. Legal information	12
13. Contents	14

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