Product data sheet

1. General description

Silicon Carbide MOSFET in a TSPAK plastic package with top side cooling structure, designed for high frequency, high efficiency systems.





2. Features and benefits

- · Top side cooling structure
- · Kelvin source configuration
- Low specific on-resistance
- Optimized dynamic performance
- Robust gate design
- 0V turn-off V_{GS} for simple gate driver
- 100% UIS Tested
- Easy to parallel
- RoHS compliant



3. Applications

- Switching mode power supplies
- UPS and energy storage systems
- · Battery formation instrument
- PV MPPT and inverters
- EV Chargers
- Welding machines
- Motor Drives

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		1200			V
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C			134		Α
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C			592		W
T _j	junction temperature				-55 to 17	'5	°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 50 \text{ A}; T_j = 25 \text{ °C}$		-	20	-	mΩ
		V _{GS} = 18 V; I _D = 50 A; T _j = 25 °C		-	16.3	29	mΩ
Dynamic	characteristics		J			,	
Q _{G(tot)}	total gate charge	$I_D = 50 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	215	-	nC
Q_{GD}	gate-drain charge	$T_j = 25 ^{\circ}\text{C}$		-	32	-	nC
Source-d	rain diode		1			1	1
Q_r	recovered charge	I_{SD} = 50 A; di/dt = 500 A/ μ s; V_{DS} = 400 V; T_{j} = 25 °C		-	276	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	SS	source sense		
3-7	S	source		G
mb	D	mounting base; connected to drain	1 2 3 4 5 6 7	SS Sym301 S

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M20120TB	TSPAK	WNSC2M20120TB6J	Reel	600	TSPAKH	06-Dec-2024

7. Marking

Table 4. Marking codes

Type number	Marking codes
WNSC2M20120TB	WNSC2M
	20120TB

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		1200	V
$V_{\rm GS,max}$	gate-source voltage			-12 to 24	V
$V_{GS,op}$	gate-source voltage			-4 to 18	V
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C		592	W
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C		134	Α
		V _{GS} = 18 V; T _{mb} = 100 °C		95	Α
I _{DM}	peak drain current	pulse width t _p limited by T _{jmax}	Fig.17	260	Α
Is	continuous diode current	V _{GS} = -4 V; T _{mb} = 25 °C		86	А
I _{SM}	pulse diode current	V_{GS} = -4 V; pulse width t_p limited by T_{jmax}		260	А
E _{as}	single pulse drain-to- source avalanche	$I_{AS} = 30 \text{ A}; L = 1 \text{ mH}; V_{DD} = 100 \text{ V};$ $T_j = 25 \text{ °C}$		450	mJ
T _{stg}	storage temperature			-55 to 175	°C
T _j	junction temperature			-55 to 175	°C
$T_{\text{sld}(M)}$	peak soldering temperature			245	°C

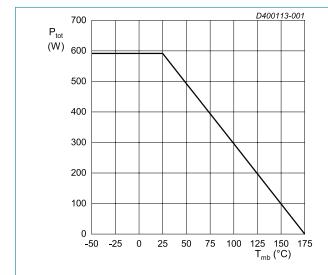


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

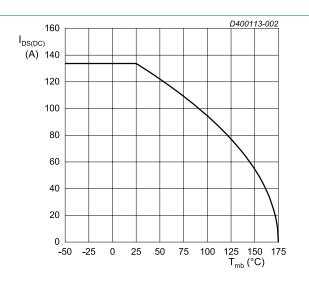


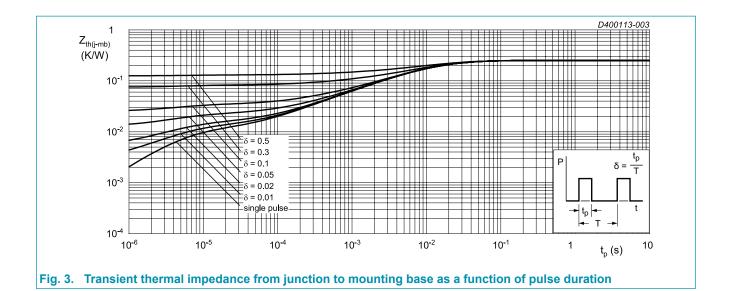
Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	0.25	-	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W

Note: Device is ESD sensitive. Handling precautions are recommanded.



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	$I_D = 20 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		1.9	2.6	3.5	V
	voltage	$I_D = 20 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 175 \text{ °C}$		-	1.9	-	V
I _{DSS}	drain leakage current	$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.2	100	μA
		$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$		-	2	-	μA
I _{GSS}	gate leakage current	$V_{GS} = 24 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	10	100	nA
		$V_{GS} = -12 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	10	100	nA
R _{DS(on)}	drain-source on-state	$V_{GS} = 15 \text{ V}; I_D = 50 \text{ A}; T_j = 25 \text{ °C}$		-	20	-	mΩ
	resistance	$V_{GS} = 18 \text{ V}; I_D = 50 \text{ A}; T_j = 25 \text{ °C}$		-	16.3	29	mΩ
		$V_{GS} = 18 \text{ V}; I_D = 50 \text{ A}; T_j = 175 \text{ °C}$		-	27.6	-	mΩ
R _G	gate resistance	f = 1 MHz; T _j = 25 °C		-	0.6	-	Ω
g _{fs}	transconductance	$V_{DS} = 20 \text{ V}; I_{D} = 50 \text{ A}; T_{j} = 25 \text{ °C}$		-	32	-	S
Dynamic	characteristics						-
Q _{G(tot)}	total gate charge	$I_D = 50 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	215	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C		-	83	-	nC
Q_{GD}	gate-drain charge			-	32	-	nC
C _{iss}	input capacitance	V _{DS} = 1000 V; V _{GS} = 0 V; f = 1 MHz;		-	4701	-	pF
C _{oss}	output capacitance	T _j = 25 °C		-	199	-	pF
C _{rss}	reverse transfer capacitance			-	20	-	pF
E _{oss}	Coss stored energy			-	100	-	μJ
t _{d(on)}	turn-on delay time	$V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V}; R_{G(ext)} = 5.1$		-	32	-	ns
t _r	rise time	$Ω$; $I_D = 50 A$; $L = 100 \mu H$; $T_j = 25 °C$		-	38	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	67	-	ns
t _f	fall time			-	16	-	ns
E _{on}	turn-on energy (Sic Diode FWD)		Fig.20	-	653	-	μJ
E _{off}	turn-off energy (Sic Diode FWD)		Fig.20	-	347	-	μJ
E _{on}	turn-on energy (Body Diode FWD)		Fig.20	-	800	-	μJ
E _{off}	turn-off energy (Body Diode FWD)		Fig.20	-	324	-	μJ
Source-di	rain diode						
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _{SD} = 25 A; T _j = 25 °C		-	2.9	-	V
		V _{GS} = -4 V; I _{SD} = 25 A; T _j = 25 °C		-	4.7	-	V
		V _{GS} = -4 V; I _{SD} = 25 A; T _j = 175 °C		-	4.1	-	V
t _{rr}	reverse recovery time	$I_{SD} = 50 \text{ A}$; di/dt = 500 A/ μ s; $V_{DS} = 400 \text{ V}$;		-	54	-	ns
Q _r	recovered charge	T _j = 25 °C		-	276	-	nC
I _{rrm}	reverse recovery current			_	9	-	Α

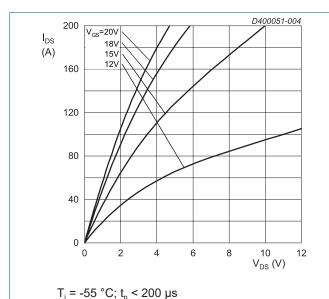


Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values

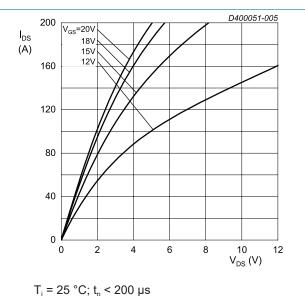
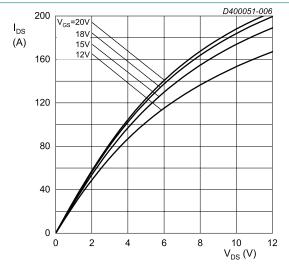
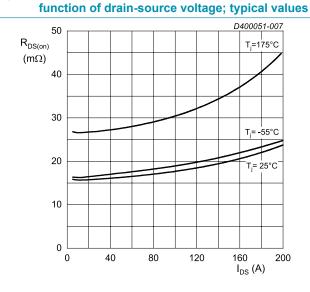


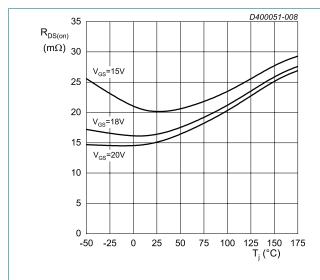
Fig. 5. Output characteristics; drain current as a



 $T_i = 175 \, ^{\circ}\text{C}; t_p < 200 \, \mu\text{s}$ Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

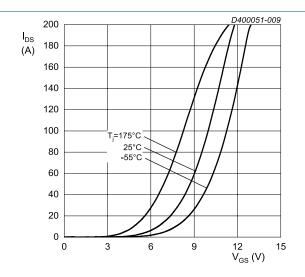


 V_{GS} = 18 V; t_p < 200 μs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



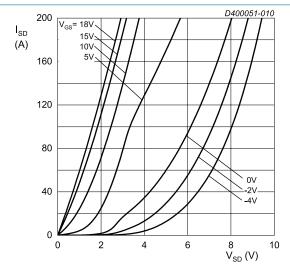
 I_{DS} = 50 A; t_p < 200 μ s

Fig. 8. Drain-source on-state resistance as a function of junction temperature



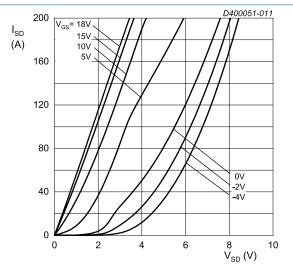
 V_{DS} = 20 V; t_p < 200 μ s

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values



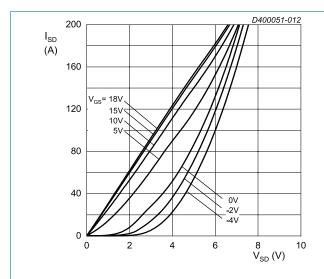
 $T_j = -55 \,^{\circ}\text{C}; t_p < 200 \,\mu\text{s}$

Fig. 10. Body diode forward characteristics; typical values

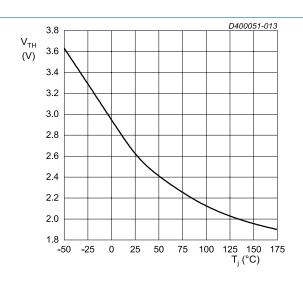


 $T_j = 25 \, ^{\circ}C; \, t_p < 200 \, \mu s$

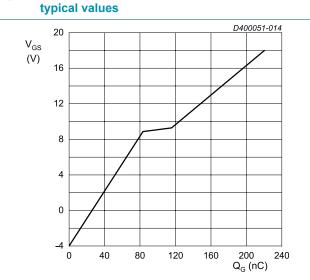
Fig. 11. Body diode forward characteristics; typical values



 T_{j} = 175 °C; t_{p} < 200 μs Fig. 12. Body diode forward characteristics;



V_{DS} = 10 V; I_{DS} = 20 mA Fig. 13. Threshold voltage as a function of junction temperature



I_{DS} = 50 A; I_{GS} = 0.1 mA; V_{DS} = 800 V; T_j = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

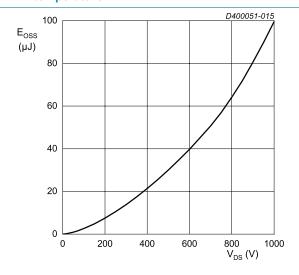
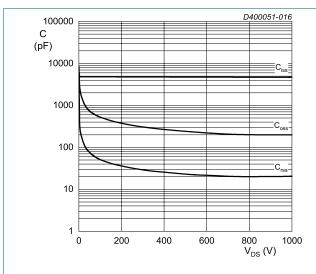


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



 $V_{DS} = 0 - 1000 \text{ V}$

 T_j = 25 °C; V_{AC} = 25 mV; f = 1 MHz

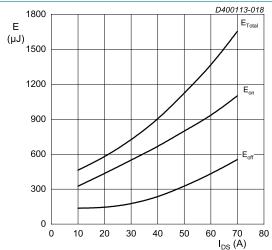
100 Limited by R_{DS(ON)}
100 100 1000 10000 V_{DS} (V)

 $T_j = 25 \,^{\circ}\text{C}; D = 0$ Parameter: t_p

1000

Fig. 17. Forward bias safe operating area

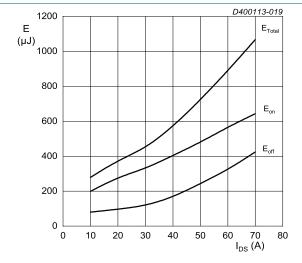




 T_{j} = 25 °C; V_{DD} = 800 V; $R_{G(ext)}$ = 5.1 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH

FWD = WNSC2M20120TB

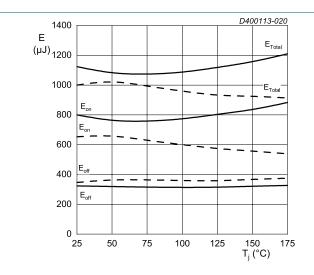
Fig. 18. Clamped Inductive Switching Energy as a function of drain current



 T_{j} = 25 °C; V_{DD} = 600 V; $R_{G(ext)}$ = 5.1 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH

FWD = WNSC2M20120TB

Fig. 19. Clamped Inductive Switching Energy as a function of drain current

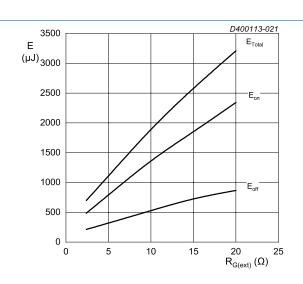


 $I_{DS}=50$ A; $V_{DD}=800$ V; $R_{G(ext)}=5.1~\Omega;$ $V_{GS}=-4$ V/18 V; $L=100~\mu H$

FWD = WNSC2M20120TB

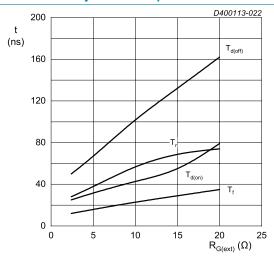
FWD = WNSC2D401200TB(- - -)

Fig. 20. Clamped Inductive Switching Energy as a function of junction temperature



 T_{j} = 25 °C; V_{DD} = 800 V; I_{DS} = 50 A; V_{GS} = -4 V/18 V FWD = WNSC2M20120TB; L = 100 μH

Fig. 21. Clamped Inductive Switching Energy as a function of external gate resistance



 T_{i} = 25 °C; V_{DD} = 800 V; I_{DS} = 50 A; V_{GS} = -4 V/18 V FWD = WNSC2M20120TB; L = 100 μ H

Fig. 22. Switching time as a function of external gate resistance

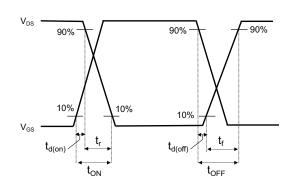
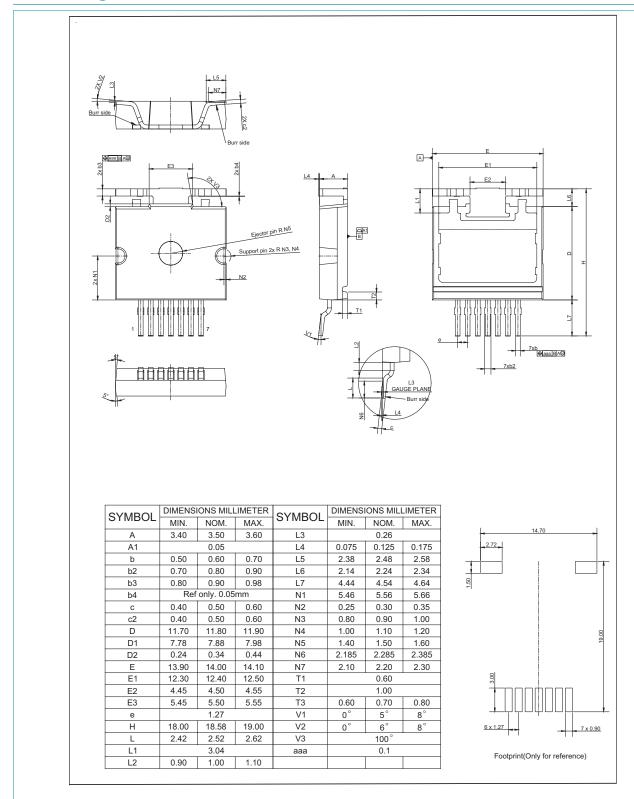


Fig. 23. Switching time definition

11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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